



SOAP II for the IBM 650 Data Processing System

# MINOR REVISION (July 1957)

This edition, Form 32-7646-1, is a minor revision of the original edition but does not obsolete Form 32-7646. The changes in this edition are:

Page	Subject
32	Three corrections in the 533 wiring diagram
33	Two corrections in the 407 wiring diagram
37	Correction in format of optimizing table entries
52	Correction of subscripts in the flow chart
60	Correction of wording in the flow chart
71	Correction of two lines in listing of optimizing table
72	Correction of one line in listing of optimizing table
86	Correction of one line in listing of constants
92	Correction of three words in the seven-words per card listing
94	Correction of one word in the seven-words per card listing

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### PART I: PROGRAMMER'S GUIDE

### Introduction

The principal achievement of an assembly program is that it almost completely relieves the programmer of the problem of assigning actual storage locations to instructions or quantities manipulated by the program. If, in the course of programming, he wishes to refer to any location in storage which will contain some quantity used by the program, he gives this location a "name", preferably one of high mnemonic value, and refers to it by means of this name. Thus, salary might be stored in "WAGES", sin x in "SIN X". The assembly program assigns actual locations to these "names" and produces an absolute machine language program.

SOAP II is a symbolic assembly program for the IBM 650 Data Processing System. It is designed to assemble programs written for any array of equipment including tapes, printer, immediate access storage, indexing registers, floating point, disk storage and inquiry stations. The assembly program itself uses none of these features. It will operate on a basic 650 having an alphabetic device. Special features described in the sequel should render it equally useful for both scientific and commercial applications.

### Definition of Terms

L = location

D = data (address)

I = instruction (address)

Tag = character used to specify an indexing register

FWA = first word address

LWA = last word address

OP = operation (code)

Block = a consecutive group of drum (or core) locations

Region = a block designated by an alphabetic character

Pseudo-instruction = an instruction which is never executed

Symbolizer part = left-most position of L, OP, D or I field

Absolute part = L, OP, D or I field with symbolizer part deleted

Available = subject to selection as an optimum drum location whenever the assembly program is given freedom of choice

Unavailable = not subject to the above selection

Reserve = to make unavailable

I. A. S. = Immediate Access (core) Storage

### SOAP II Coding Form and Input Card Format

Figure 1 shows the coding form to be used when writing for SOAP II. Vertical dotted lines separate the symbolizer and absolute parts of the L, OP, D and I fields. Observe that the input card format is indicated at the top of the form. Punching in columns 1-40 and 73-80 is ignored except for column 2 which must not contain a Y(12) punch.

Instructions and data enter the assembly one word per card. The output is in the form of one word per card load cards.

# Specifying Program Area on Drum

Prior to assembly, the entire drum is available to the program. It is invariably necessary, however, to prevent the program from occupying certain drum locations, e.g., input-output blocks, tables, data areas, etc. There are three pseudo operations which can be used to restrict the assembled program to any predesignated parts of the drum:

### Block Reservation: BLR

This card contains two absolute drum addresses FWA, LWA (FWA ≤ LWA) punched in the absolute part of the D and I fields, respectively. All locations from FWA to LWA (inclusive) are made unavailable.

For example,

COL.	41	42	43	44-	-47	48	49,	50	51	52-	-55	56	57	58-	-61	62	6373
,	TYPE	9-6Z		LOCATIO	ON		RATI			DATA ADDRES	ss	TAG		STRUCT ADDRES		⊢∢G	REMARKS
						$\mathbb{B}$	<u>L</u> 1	3		150	0			190	0		

will make drum locations (1500-1900) unavailable.

### Block Availability: BLA

This card is exactly like a BLR card except that it makes all locations from FWA to LWA <u>available</u>. Note that it is generally redundant to put a BLA card at the beginning of an assembly since the entire drum is already available.

By way of illustration,

COL.	41	42	43	44-	-47	48	49,	50	51	52-	-55	56	57	58-	-61	62	63		-72
	->au	20-0		LOCA	TION		RAT GODE			DATA ADDRES	ss	T A G		STRUCT ADDRES		TAG		REMARKS	
				<u> </u>		B	L	Α		160	0			173	0				

# 650 SOAP IL CODING FORM

ADDRESS 6	ADDRESS G LOWER 8002 LOWER 8002													-
							ij							

Figure 1

3

will make drum locations 1600-1730 available.

## Regional Specification: REG

This card contains an alphabetic character, punched in the symbolizer part of the D field, to be associated with a block defined by the terminal drum addresses FWA, LWA. As in the case of a BLR card, all locations from FWA to LWA are made unavailable. If the programmer wishes to refer to locations within a region using "regional" addresses (see below), an REG card must precede the first use of such addresses.

The REG card

COL.	41	42	43 4447	48 49, 50	51 5255	56	57 5861	62	637:
		0-0z	LOCATION	OPERATION CODE	DATA ADDRESS	T A G	INSTRUCTION ADDRESS	TAG	REMARKS
				REG	P0027		0036		

defines the block 0027-0036 as region P and causes this block to be made unavailable.

## Types of Addresses

### Regional

A regional address has the form

where "A" is any alphabetic character and NNNN is a four digit number. The regional address of the <u>first</u> word of region B, for example, is B0001. The twelfth word of region R is R0012.

### Note:

- 1. The regional address "A" 0000 is permitted. It will result in the absolute address (FWA-1).
- 2. Regional addresses whose absolute equivalents are larger than LWA are permitted. For example, if region R is the block 1951-1960, the regional address R0015 will assemble as 1965.
- 3. The use of a regional address without a defining REG card will result in the assembled address being left blank.

### Symbolic

Symbolic addresses have the form

$$\begin{bmatrix} \mathbf{C}_5 & \mathbf{C}_4 & \mathbf{C}_3 & \mathbf{C}_2 & \mathbf{C}_1 \end{bmatrix}$$

where the C's may be any characters acceptable to the alphabetic or special

character devices subject to the following restrictions:

- 1.  $C_5$  must not be blank.
- 2. An address meant to be symbolic must not have a regional form, i.e., an alphabetic character followed by four numerical characters.

Typical symbolic addresses are

1	1	! !	1	-		
	X			N	ET	
	X	Y	(	G	ROSS	
	2	GS	'	Т	AXES	
	L	OOP		В	ONDS	
	F	INIS		5		
	R	EAD	1	F	IVE	

Symbolic addresses are assigned optimum drum equivalents when initially encountered and maintain this equivalence throughout the assembly unless redefined. It is possible to preassign a value to a symbol (see EQU, SYN, pages 13, 14.)

### Absolute

Absolute addresses have the form

NNNN

where NNNN is a valid four digit machine address. The symbolizer part of an absolute address must be blank.

Drum locations corresponding to absolute addresses are <u>not</u> made unavailable during assembly. Thus, drum locations containing instructions or data specified by absolute addresses must be block reserved at the beginning of assembly.

### Blank

Whenever the D or I address of an instruction refers to the location of the "next" instruction (or data) written on the coding form, this address and the location of the "next" instruction may be left blank provided this order is not altered when assembling. Blank addresses will be filled in optimally by the assembly program.

The D and I address of an instruction may both be blank if they both refer to the location of the next instruction written. An address should <u>not</u> be left blank in an instruction card unless the above meaning is specifically intended.

### Operation Codes

650 operation codes may be written in either three character symbolic or two digit numerical form, e.g.,

Symbolic operation codes to be used with SOAP II are given in the Appendix.

Any card containing an illegal symbolic op code will be considered an instruction and processed accordingly. The assembled op will be left blank.

### Numerical Data

Numerical data are written using the absolute part of the OP, D and I fields, e.g.,

COL.	41	42	43 4	14-	-47	48	49,	50	51	52-		-55	56	57	58-	•	-61	62	63-	-72
	TYALE	a-02	L	OCATI	ON		RATI	-			ATA DRES		ΤAG		STRL			T ▲ G	REMARKS	
			P:	[			3	I		4	15	9			26	55	4			
			ŌI	٧E			0	0		0	00	) I		1	QC	00	0			
			F	IC,	4		0	0		Q	00	00		- 1	42	20	0			

The symbolizer parts of these fields must be blank. Leading or trailing zeros must be punched in the card.

# Alphabetic Data: ALF

The pseudo-op card ALF permits the programmer to enter alphabetic information into the 650 on load cards, up to five characters per card. Any characters acceptable to the alphabetic or special character devices are permissible, and are written in the D field. During assembly, alphabetic data are converted to their numerical equivalents and the latter are punched as a ten digit number in exactly the same manner as numerical data. For example,

COL.	41	42	43 4447	48 49, 50	51 5255	56	57 5861	62	6372
	TYPE	0-0Z	LOCATION	OPERATION CODE	DATA ADDRESS	TAG	INSTRUCTION ADDRESS	TAG	REMARKS
			EDPM	ALF	IBMNY				

will cause the number [69, 62, 74, 75, 88] to be loaded into symbolic location EDPM.

Similarly,

COL.	41	42	43	44-	-47	48	49,	50	51	52-	-55	56	57	58-	-61	62	2 637
	->au	9-9Z		LOCATI	ON		RATI			DATA ADDRES		† AG		STRUC ADDRE		TAG	REMARKS
				90	0	Α	L	F		705	5						

will load [00, 97, 90, 95, 00] into (core) location 9010.

Note: The I address of an ALF card is not used by SOAP II. However, if it is blank, it will be filled in by the symbol "SOAP 2". This is merely an artifice employed to avoid erroneous error detection when listing output (See Appendix-407 Control Panel).

## Signs

Any punch in column 42 signifies a negative instruction or negative data. For coding purposes, a (-) written in the sign column and punched as X (11) will suffice. A blank in column 42 is interpreted as positive.

### Comments Card: Type 1

Every card processed by SOAP II may contain remarks not exceeding 10 characters in length. When longer remarks are desired, a comments card, which may contain up to 30 characters, may be used. Comments cards have no effect on the assembly. They are merely reproduced in the output deck. The following figure shows the format for comments cards. Note that the type (1) is punched in column 41.

COL.	41	42	43 4447	48 49, 50	51 5255	56	57	5861	62	63-			-72
	TYPE	5-GX	LOCATION	OPERATION CODE	DATA ADDRESS	T A G		ISTRUCTION ADDRESS	TAG		REMA	RKS	
			<b>≺</b> ⊢ C	Ю	M	M		E		N	<u></u>	S	>

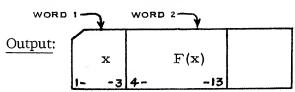
### Example.I

Prepare a table of

$$F(x) = Ax^2 + Bx + C$$

for

$$x = 1, 2, \dots, 100$$
 assuming A, B, C integers and  $|F(x)| < 10^{10}$ .



Coding is given in Figure 2. The assembled program is shown in Figure 3.

# 650 SOAP IE CODING FORM

Н							
E & LOCATION	N CODE	DATA ADDRESS	A ADDRESS	REMARKS	DPPER 8003 LOWE	JLATOR LOWER 8002	DISTRIBUTOR
EXAMP	P L E	1 b CAL	CULATE	F OF X			
				-			
	BLR	1951	0961	READ AREA			
	REG	P 0027	0028	PUNCH AREA			
SETX	R AU	ONE	STX	SET X			
、STX	SITU	100001			×		
,	MPY	A		CALCULATE		AX	
	AILO	B		1		Ax+B	
	RAU	8002		*	AX+B		
	MPY	P10001				(AX+B)X	
	ALOC	Ü				(AX+B)X+C	
	SITL	P 0002					
	РСН	P 0001		PUNCH			
	RAU	RAU PIOOOI		IS X MAX	×		
	SUP	SUP X MAX			001-X		
	NZU		6666				
	AUP	101	STX	STEP X	-+×		
O'NE	00	0000	1000	CONSTANTS			
XMAX	00	0000	0010				
0	00	0000	1010				
	_						

Figure 2 : Example 1

PROBLEM.

H PUNCH AREA READ AREA CONSTANTS CALCULATE IS X MAX STEP X × PUNCH SET X T0 1 P EXAMPLE 1 CALCULATE STX STX P0027 P0001 P0002 P0001 P0001 P0001 XMMX ONE REG BLR AUP ALO ALO PCH RAU SUP RAU STU MPY RAU МРΥ STL NZN XMAX SETX ONE STX 

0.005

Figure 3: Assembly of Example 1

# 650 SOAP IL CODING FORM

				21-			
LOCATION	OPERATION	DATA ADDRESS	INSTRUCTION G ADDRESS	T REMARKS	ACCUM UPPER BOOS	ACCUMULATOR	DISTRIBUTOR
EXAMP	<b>3</b> 7	2 b CAL	CULATE	GROSS PAY			
	BLR	1951	1960	READ AREA			
	REG	P 0027	0030	PUNCH AREA			
_	BLR	0000	6000	RATE TABLE			
_							
READ	RICD	1950		READ CARD			
	LDD	1951		STORE			TDENT
	STD	P 0001		IDENT			
	RAL	1952		GET		000 X 000	
	STD	P 0002	-	HOURLY		RAU DOOX CE	
	ALOI	MASK	8002	RATE		lander of the	
8002	RAU	6000	CG	1	RATE = X.XX		
D D	МРУ	1953		CALCULATE	1	GROSS=XXX•XXX	Houds - XX - X
	SITDI	P 0003		GROSS			
	RD	1000		ROUND		xx•xx	
	STL	P 0004					
	P CH 1	P 0001	READ	PUNCH			
MASK	R AU	0000	C G	CONSTANT			
		-					
		_					

Figure 4: Example 2

CODER

OF

EM

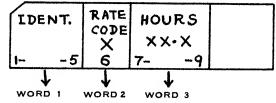
						0013	0054	0800	0057	0031	8002	6900	900	0032	6600	6600	0020		6900
			gagger 1 and			1950	1951	0027 0	1952 (	0028 (	9 4500	6000	1953 (	0029 (	0001 (	0600	0027 (		0000
						70	69	24 (	65	54 (	15	09	13	24 (	31 (	20 (	71 (		09
						00 20	0013	0054	0080	1500	0031	8002	0063	0024	0032	0039	0033		0034
GROSS PAY		READ AREA	PUNCH AREA	RATE TABLE		READ CARD	STORE	IDENT	GET	HOURLY	RATE		CALCULATE	GROSS PAY	ROUND		PUNCH		CONSTANT
2 CALCULATE GROSS PAY		1960	0030	6000							8002	9)					READ		90
EXAMPLE		1951	P0027	0000		1950	1951	P0001	1952	P0002	MASK	6000	1953	P0003	0001	P0004	P0001		0000
_		BLR	REG	BLR		RCD	TDD	STD	RAL	STD	ALO	RAU	MPY	STD	SRD	STL	PCH		RAU
						READ						8002	90						MASK
<del></del> 1	g=4				<b>-</b> -1													<b>-</b>	
<del>, , ,</del>	2	W	4	ιυ	9	7	ω	6	10	11	12	13	14	15	16	17	18	19	20

Figure 5 : Assembly of Example 2

### Example II

Given a rate code and number of hours worked, search a rate table and calculate gross pay.

Input:



Word 1: 00000, xxxxx = Identification Word 2: 00,000x,0000 = Rate Code Word 3: 00 00000 xx.x = Hours

The rate code is a one digit number 0-9. The corresponding hourly rate (x.xx) is in the low three positions of locations 0000-0009.

Output:

WOR	ם 1 •	WORD 2	word 3 ↓	WORD 4	
IDE	NT.	RATE	HOURS	GROSS	
		X	XX•X	×××·××	
1-	-5	6	79	1014	

Coding is shown in Figure 4. The assembled program is given in Figure 5.

# Indexing

When the D or I address of an instruction is to be modified by an indexing register, the corresponding TAG column is used to indicate the appropriate indexing register. Permissible tags are A, B, C or 1, 2, 3. The assembly program will automatically add the proper multiple of 2,000 or 200 to a tagged address.

For example, the following program will perform the vector addition  $\mathbf{x_i} + \mathbf{y_i} = \mathbf{z_i}$ ,  $i = 1, 2, \ldots, 100$ . The  $\mathbf{x_i}$  are stored in region X,  $\mathbf{y_i}$  in region Y and  $\mathbf{z_i}$  in region Z.

COL.	41	42	43	4447	48	49,	, 50	51	52-	-55	56	57	58-	-61	62	63-	-72
	- YPE	20-0		LOCATION	OP	E R AT C O D		×-	DA ADD	TA RESS	T A G	IN	STRUC ADDRE	TION	T A G	REMARKS	
					R	S	Α		01	00		A	DD				<del></del>
П	•		A	DD	R	A	L	X	01	01	A		1				
					Α	L	Q	Y	01	01	A						
				 	S	T	L	Z	01	01	Α						
			1	1	A	X	A	è	OC	001							
4	_		ا		N	Z	A	Α	DI	)			P				

The following types of D or I addresses will  $\underline{not}$  be indexed even if a tag is present:

- 1. Undefined symbolic
- 2. Blank
- 3. Not in the range 0000-1999 or 9000-9059.

# Predefining Symbols

The following two pseudo operations may be used to preassign a value to a symbolic address:

Equivalence: EQU

The symbol written in the D address is assigned the equivalent of the expression written in the I address. The I address may be absolute, regional or symbolic. If the I address is regional or symbolic, it must have been previously defined.

The EQU card

COL.	41	42	43	44-	-47	48	49,	50	51	52-	-55	56	57	58-	-61	62	63-	-72
L	TYPE	8-67		LOCAT	ION	1	RATI			DATA ADDRES		T A G		STRU ADDF	CTION	TAG		REMARKS
	Ť	Ï				E	Q	Ū	T	APE	.			80	010			

will cause the symbol TAPE 1 to be assigned the value 8010 whenever it subsequently appears during assembly.

The EQU card

COL.	41	42	43	44-	-47	48	49,	50	51	52-	-55	56	57	58-	-61	62	2 6372
1	HO41	ω− <b>⊌</b> Σ		LOCATIO	ON		RAT			DATA ADDRES		TAG		STRUC ADDRI		TAG	
						E	Q	U	C	ŌDE	-		R	00	02		

will cause the symbol CODE to be assigned the value R0002, i.e., the second word of region R. Region R must have been previously defined by an REG card.

Similarly, the EQU card

COL.	41	42	43 44-	-47	48 49, 50	51	5255	56	57	58-	-61	62	63-	-72
	140 E	ω- <b>6</b> 2	LOCAT	ION	OPERATION CODE		DATA ADDRESS	TAG	IN	STRUCT ADDRES	ION S	TAG	RE	MARKS
Ì		Ï			EQU	A	LPHA		B	ETA	١			

will assign to ALPHA the value previously assigned to BETA.

### Synonym: SYN

An SYN card is exactly like an EQU card except that the equivalent of the expression written in the I address must be a drum address, i.e., 0000≦ "I" ≦ 1999. This drum location is made unavailable to the program. The I address may be absolute, regional or symbolic.

The SYN card

001	4.	4.0	14-	4.4		_				<del></del>				,					
COL.	41	42	43	44-	-47	48	49,	50	51	52	-55	56	57	58-	-61	62	63-		-72
	Ţ	ş							_			T	_	<u> </u>		-			- / 2
	Đ	ĠN		LOCATI	ON		CODE			DATA ADDRES	s	Å	IN	STRUCTI ADDRESS	ON i	A		REMARKS	
						S	Y	N	E	NTR'	Y			120	5				*

will assign the value 1205 to the symbol ENTRY and reserve location 1205.

If the I address of an EQU or SYN card is undefined, the card will be ignored. EQU or SYN takes precedence over any previous definition.

# Assembly Capacity and Speed

During assembly, all symbolic addresses and their equivalents are stored in a symbol table which can accommodate 400 symbols. Programs containing more than 400 symbolic addresses should be assembled in separate blocks. Non-overlap can be guaranteed by use of an availability punchout described in the next section.

Assembly progresses at the rate of 50 to 90 cards per minute. This will decrease somewhat if the symbol table becomes densely packed or if few drum locations are available for the remaining program.

# Punching Availability Table: PAT

The availability table is a 200 word table used by SOAP II to "remember" those drum locations already used and those still remaining. The pseudo-op PAT can be used at any time during assembly to cause punching of this table in a highly readable form. The output will be fifty Type Y (12) cards, one for each dynamic drum level (00-49). These cards may be listed with the 407 control panel described in the Appendix. This listing will reveal at a glance all drum locations used by the program (0) and those remaining (1) for additions or corrections.\*

The availability table may be loaded as input to an assembly thus restoring the \*See page 91.

availability status which existed at some point of a prior assembly.

# Order of Assembly Deck

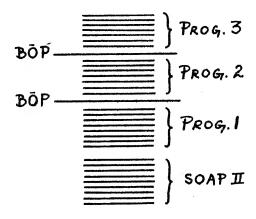
The assembly input deck should have the following order:

- 1. SOAP II
- 2. Deck to be assembled

Due to the one pass nature of the assembly, priority for the choice of optimal locations diminishes as the assembly progresses. Thus, frequently executed portions of the program should be placed toward the beginning of the assembly deck.

# Multifile Assembly: BŌP

Any number of independent programs may be assembled in one pass. Merely place a pseudo-op card  $B\overline{OP}$  (Beginning of Program) between each program, i. e.,



Note that it is redundant to place a  $B\overline{O}P$  card before the first of one or more programs to be assembled.

## Machine Operator's Guide

- 1. 533 Read-Punch Unit
  - a. Insert SOAP II control panel
  - b. Ready read feed with assembly deck
  - c. Ready punch feed with blanks
- 2. 650 Console
  - a. Set programmed switch to STOP
  - b. Set half cycle switch to RUN

- c. Set control switch to RUN
- d. Set display switch to DISTRIBUTOR
- e. Set overflow switch to SENSE
- f. Set error switch to STOP
- 3A. If SOAP II is being loaded, set (70, 1951, 9999) in storage entry switches.
- 3B. If SOAP II is already on the drum, do one of the following:
  - 1. Set (00, 0000, 1000) in storage entry switches.
  - 2. Precede input with a BOP card and set (00, 0000, 1950) in storage entry switches.
- 4. Press computer-reset key
- 5. Press program-start key
- 6. When read hopper empties, press end of file key
- 7. The availability table may be obtained manually by sending control to location 1900.

# Programmed Stops

- 0111-Symbol table full; pressing program-start key will continue assembly. Symbolic address which cannot be assigned an equivalent will be left blank in the output.
- 0222-Drum packed, i.e., no locations available for remaining program.

  Pressing program-start key will continue assembly. Addresses which cannot be assigned will be left blank in the output.

There are no other programmed stops.

### **APPENDIX**

# Heading: HED

Heading is used to avoid duplicity of symbols when several programs or several sections of a single program are to be assembled together. The need for heading is paramount if several persons have contributed to a program or when a program employs symbolic library routines.

Heading is accomplished in SOAP II by the automatic insertion of a heading character into the <u>right-most</u> position of symbols having this position blank. The heading character is punched in the symbolizer part of the D address of a HED card. Thus the symbol

$$T | W \overline{O} b b |$$
 (b = blank)

subsequent to the heading card

COL.	41	42	43 44	47	48 49	, 50	51	52-	-55	56	57	58-	-61	62	2 6372
	TYPE	SIGN	LOCATION	,	OPERAT			DATA ADDRES	s	T A G		STRUCT ADDRES		TAG	REMARKS
					HE	$\overline{\mathbb{D}}$	Z	1							

is processed by the assembly program as if it were actually the symbol

In order to make effective use of heading, it is necessary to agree before hand to restrict all symbols to <u>four</u> characters or less.\* Prior to assembly, each of the various sections of the program is preceded by an appropriate HED card.

If within a section of the program headed by "A" it is desired to refer to the symbol (TAX bb) appearing in a section headed by F, simply use the symbol (TAX bF). The small b, for blank column, is necessary to prevent the card punch operator from closing up the symbol and thus taking the "F" out of the heading column.

<sup>\*</sup>If a symbol is to be referred to by several headed parts of the program, then it is best that this symbol not be headed anywhere. This can be accomplished by making it a five-character symbol.

The effect of a heading card is terminated only by another heading card. If it is desired to terminate a heading card without introducing a new heading, use a HED card with a blank D address. Any character acceptable to the alphabetic or special character devices may be used for heading.

# 800X Instructions

When the D or I address of an instruction located in 800X (X = 0, 1, 2, 3, 5, 6, 7) is to be optimized by the assembly program, an instruction located in 800X should be written on the coding form. 800X instructions will be automatically bypassed when loading the assembled output.

# Use of Library Programs

Within the framework of SOAP II, library programs are in either symbolic or relocatable form. Both of these forms are discussed in the following two sections.

# Symbolic Library Programs

Symbolic library programs are written in exactly the same form as the main program. Except for the entry and exit symbols, all symbols are four characters or less. They may thus be conveniently headed. It is important to note that such programs do not incorporate a "guaranteed" optimization.

# Relocatable Library Programs: REL

Whenever tight optimization is required, one should use relocatable library programs. These are coded in absolute and may be translated an amount specified by the programmer. This is accomplished by use of the pseudo operation REL (relocate). The number punched in the absolute D address specifies the amount of drum translation,  $\Delta_D$ . Similarly, the absolute I address specifies the amount of core translation,  $\Delta_C$ . A blank field is interpreted as zero. Thus

P G LOCATION OPERATION DATA T INSTRUCTION T ADDRESS G ADDRESS G REMARKS	COL. 41	42	43 44-	-47	48 49	, 50	51	52-	-55	56	57	58	61	62	63-		
DELLOS	YAE	9-62	LOCATIO	N					s	T A G	IN	STRUCTIO	N	T		REMARKS	-72
					RE	匚		1200	0					9			$\dashv$

would cause drum addresses in the relocatable program following it to be incremented by 1200 and core addresses by 10. The REL card

COL.	41	42	43 4447	48 49, 50	51 5255	56	57	5861	62	637	72
	一十年回	9-6Z	LOCATION	OPERATION GODE	DATA ADDRESS	T A G		STRUCTION ADDRESS	TAG	REMARKS	
				REL	0600						

will increment subsequent drum addresses by 600 and core addresses by 0. Only positive translations are permitted.

Relocatable library programs <u>must</u> be assembled <u>prior</u> to the main program. The programmer need not block reserve the drum area used by a relocatable program. This is automatically performed by the assembly program.

The effect of an REL card is terminated only by another REL card.

If a translated drum <u>location</u> is greater than 1999, it is left blank in the output. The same applies to a translated core location greater than 9059.

### Writing Library Programs

The following two sections are of primary interest to programmers responsible for writing SOAP II library routines.

### Symbolic Library Programs

Entry and exit symbols should be five characters to avoid heading difficulties. All other symbols should be four characters or less to permit heading by the user.

### Relocatable Library Programs

Relocatable programs are written in absolute and should use the lowest possible drum or core locations in the unrelocated state. The following rules apply specifically to these routines:

- 1. A relocatable card is indicated by a 2 punch in the type column (41).
- 2. Op codes may be symbolic or numerical
- 3. L may be drum or core.
- 4. D and I may be absolute or symbolic.\*

<sup>\*</sup>Symbolic D or I addresses may occasionally be required to refer to other routines.

- 5. Absolute addresses may be one of five varieties:
  - a. Non-relocatable (Fixed): |F | NNNN |
  - b. Drum relocatable: NNNN | ; 0000 ≤ NNNN ≤ 1999
  - c. Core relocatable: | NNNN | ; 9000 ≤ NNNN ≤ 9059

where NNNN is a four-digit number. In situations where D or I is relocatable but not in the range (b) or (c), the usage

- d. Drum relocatable: D NNNN
- e. Core relocatable: |C | NNNN|

will eliminate possible ambiguity.

- 6. Absolute D or I addresses in the range 0000-1999 or 9000-9059 after possible relocation may be tagged for indexing by the assembly program.
- 7. If L, <u>after possible relocation</u>, is a drum address, it will be made unavailable. D or I drum addresses are not reserved.

Two pseudo operations, RBR and REQ are designed for use with relocatable programs.

# Relocatable Block Reservation: RBR

The pseudo operation RBR is identical to BLR except that the drum addresses FWA, LWA are incremented by  $\Delta_D$  prior to reservation. For example, if  $\Delta_D$  = 1000,

COL.	41	42	43 44-	-47	48 49,	50	51	52-	-55	56	57	58-	-61	62	2 63-	-72
	TYRE	20-02	LOCATIO	N	OPERAT CODI			DATA ADDRES	ss	T A G		STRUCT ADDRES		T A G	REMARKS	
					RB	R		002	5			005	0			

will cause drum locations (1025-1050) to be reserved.

The use of RBR is mainly for drum routines which use erasable blocks within the routine. Reservation of these blocks is easily accomplished with RBR cards thus avoiding the necessity of a large number of cards simply to reserve erasable areas.

## Relocatable Equivalence: REQ

The pseudo operation REQ enables symbolic linkage of the main program with relocatable programs. REQ is similar to EQU except that the absolute I address is incremented by  $\Delta$  or  $\Delta$  prior to being assigned as the equivalent of the D

symbol in the D address. The I address must be absolute, type (b) or (c) (see 5 above). Thus if  $\Delta_C = 0020$ ,

COL. 4	ī	42	43 44-	-47	48 49, 5	0 51	5255	56	57	5861	62	2 63-	-72
TYPE	200	8-6	LOCATIO	N	OPERATIO GODE	N	DATA ADDRESS	T A G		STRUCTION ADDRESS	TAG	REMARKS	,
		-			REG	R	AMAC			9010			

will assign 9030 as the equivalent of the symbol "RAMAC".

Similarly, if 
$$\Delta$$
<sub>D</sub> = 1300,

COL.	41	42	43 4447	48 49, 50	51 52-	-55	56	57	58-	-61	62	63-	-72
	TYPE	S-62	LOCATION	OPERATION GODE		ATA DRESS	TAG		STRUCTI Addres		T A G	REMAR	ks
				REQ	TY	PE3			007	5			

will assign 1375 as the equivalent of the symbol "TYPE 3."

To illustrate a typical drum-drum\* relocatable subroutine, the program given in Figure 6, using indexing registers, will evaluate the polynomial

$$F(x) = a_n x^n + a_{n-1} x^{n-1} + \dots a_0$$

where n  $\geqq$  0 and x,  $a_i$  are floating point numbers. The  $a_i$ 's are stored such that

$$L(a_i) = L(a_0) + i$$

The calling sequence is

COL. 4	11	42	43 44	47 4	8 49, 5	0 51	52	55 56	57	58	61	62	6372
	TYPE	2-62	LOCATION	OF	CODE	*	DATA ADDRESS	T A	IN	STRUCTION ADDRESS	N	T A G	REMARKS
				F	RAL	C	W						
			!	L	DD	X							
			!	F	RAC	N	EXT		P	ŌLYX	(		

where 
$$CW = \begin{bmatrix} 00, & \{L(a_0) + 2000\}, & n \end{bmatrix}$$
 if the  $a_i$  are on the drum  $\begin{bmatrix} 00, & \{L(a_0) + 200\}, & n \end{bmatrix}$  if the  $a_i$  are in core

The routine will exit to (drum location) NEXT with the result in the upper accumulator.

<sup>\*</sup>Loaded onto and executed from the drum.

41	$\vdash$	43 44	47	18 49, 50	51	5255	56	57	586	1162	637
PE	8-62	LOCATION	0	PERATION CODE		DATA ADDRESS	TAG	IN	STRUCTION ADDRESS	TAG	REMARKS
2		000	7 3	STD		0013	İ		0016		SAVE X
2		0016	5   1			0019			0011	T	SET ADDR. OF Qo
2		0011	3	DA	-	0019			0001	T	ADDR. OF UO
2		0001	F	AA S	F	8002			0010	T	n→ I.A. A
2		10010	F	MP	-	0013	$\exists$		0019	1	()·X
2	$\perp$	10019	F	AD	F	9999	$\exists$	-	0002		$()\cdot X + a_i \rightarrow ()$
2		0002	.	IZA	1	0005	$\dashv$	-	0000	C	IS $N=0$
2	1	10005	S	XA	F	0001	$\top$		0010		n-1→n
2		10013				0000	1		0000		ERASEABLE (X)
			R		_	DLYX			0009		DEFINE ENTRY

Figure 6: Relocatable Drum-Drum Subroutine

The relocatable drum-core\* subroutine given in Figure 7 will evaluate K! where K > 0 and K! <  $10^{10}$ . The calling sequence is

COL.	41	42	43 44	47	48	49.	50	51	52-	-55	56	57	58-		-	
	Ţ	ş						_			36	57	28-	-61	162	2 637
	a.E.	20	LOC	ATION		CODE			DATA ADDRES	ss	AG	IN	STRU( ADDRI	CTION	TAG	
- 1					R	AL		K				-			Ü	× × × × × × × × × × × × × × × × × × ×
-1					L	D.	D	E	XIT	-		C	AL	KF		

The result will be in the lower accumulator on exiting from the subroutine. Note that whereas seven core locations are required for execution, only six drum locations are needed to store the subroutine. Savings of this kind may usually be effected by placing erasable locations at the <a href="end">end</a> of drum-core routines.

<sup>\*</sup>Loaded onto the drum and executed in core.

COL. 41	42	43 4447	48 49, 50	51 5255	56	57 5861	62	6372
T Y P E	9-6Z	LOCATION	OPERATION GODE	DATA ADDRESS	T A G	INSTRUCTION ADDRESS	TAG	REMARKS
2		0000	STD	9006		9001		STORE EXIT
2		0001	RIAA	F 8002		19002		K → I.A. A
2		0002	SXA	F 0001		9003		(K-I) IN I.A. A
2		0003	NZA	9004		9006		IS (K-I) = O
2		0004	RAU	F 8002		9005		( )→UA
2		0005	MPY	F 8005		9002		( )(K-P)
			RIEQ	CALKF		9000		DEFINE ENTRY

Figure 7: Relocatable Drum-Core Subroutine

# SYMBOLIC 650 OPERATION CODES

Numerical	Symbolic*	Operation
00	NOP	No Operation
01	HLT	Halt
02	UFA	Unnormalized Floating Add
03	RTC	Read Tape Check
04	RTN	Read Tape Numeric
05	RTA	Read Tape Alphanumeric
06	WTN	Write Tape Numeric
07	WTA	Write Tape Alphanumeric
08	LIB	Load I.A.S. Block
09	LDI	Load I.A.S.
10	AUP	Add Upper
11	SUP	Subtract Upper
12	<del>-</del> ,	(Not Used)
13	-	(Not Used)
14	DIV	Divide
15	ALO	Add Lower
16	SLO	Subtract Lower
17	AML	Add Magnitude to Lower
18	SML	Subtract Magnitude from Lower
19	MPY	Multiply

<sup>\*</sup>All O's in symbolic OP codes are alphabetic O's, not numerical zeros.

Numerical	Symbolic	Operation
20	STL	Store Lower
21	STU	Store Upper
22	SDA	Store Data Address
<b>2</b> 3	SIA	Store Instruction Address
24	STD	Store Distributor
25	NTS	Branch No Tape Signal
26	BIN	Branch on Inquiry
27	SET	Set I.A.S. Ring
28	SIB	Store I.A.S. Block
29	STI	Store I.A.S.
30	SRT	Shift Right
31	SRD	Shift and Round
32	FAD	Floating Add
33	FSB	Floating Subtract
34	${ m FDV}$	Floating Divide
35	SLT	Shift Left
36	SCT	Shift Left and Count
37	FAM	Floating Add Magnitude
38	FSM	Floating Subtract Magnitude
39	FMP	Floating Multiply

Numerical	Symbolic	Operation
40	NZA	Branch Non-Zero I.R. A
41	BMA	Branch Minus I.R. A
42	NZB	Branch Non-Zero I.R. B
43	ВМВ	Branch Minus I.R. B
44	NZU	Branch Non-Zero Upper
45	NZE	Branch Non-Zero
46	ВМІ	Branch Minus
47	BOV	Branch on Overflow
48	NZC	Branch Non-Zero I.R. C
49	BMC	Branch Minus I.R. C
50	AXA	Add I.R. A
51	SXA	Subtract I.R. A
52	AXB	Add I.R. B
53	SXB	Subtract I.R. B
54	NEF	Branch No End of File
55	RWD	Rewind
56	WTM	Write Tape Mark
57	BST	Backspace Tape
58	AXC	Add I.R. C
59	SXC	Subtract I.R. C

Numerical	Symbolic	Operation
60	RAU	Reset Add Upper
61	RSU	Reset Subtract Upper
62	-	(Not Used)
63	-	(Not Used)
64	DVR	Divide Reset Upper
65	RAL	Reset Add Lower
66	RSL	Reset Subtract Lower
67	RAM	Reset Add Magnitude into Lower
68	RSM	Reset Subtract Magnitude into Lower
69	LDD	Load Distributor
70*	RD1	Read Input Storage 1
71*	WR1	Write Output Storage 1
72	RC1	Read Conditional Input Storage 1
73	RD2	Read Input Storage 2
74	WR2	Write Output Storage 2
75	RC2	Read Conditional Input Storage 2
76	RD3	Read Input Storage 3
77	WR3	Write Output Storage 3
78	RC3	Read Conditional Input Storage 3
79	RPY	Reply

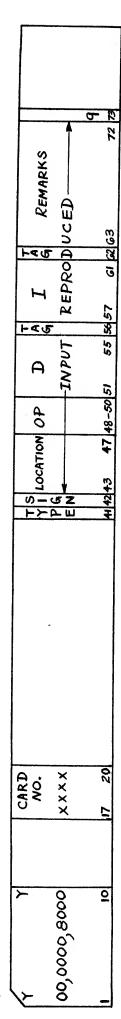
<sup>\*</sup>SOAP II will also accept the symbolic op codes

<sup>70</sup> RCD Read Card 71 PCH Punch

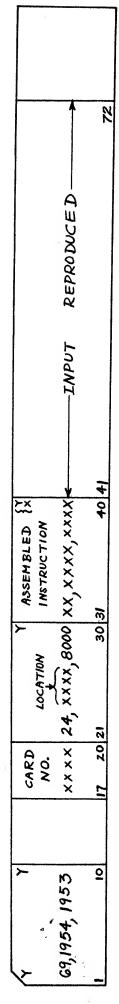
Numerical	Symbolic	Operation
80	RAA	Reset Add I.R. A
81	RSA	Reset Subtract I.R. A
82	RAB	Reset Add I.R. B
83	RSB	Reset Subtract I.R. B
84	TLU	Table Lookup
85	SDS	Seek Disk Storage
86	RDS	Read Disk Storage
87	WDS	Write Disk Storage
88	RAC	Reset Add I.R. C
89	RSC	Reset Subtract I.R. C
90	BDO	Branch on 8 in Distributor 10
91	BD1	Branch on 8 in Distributor 1
92	BD2	11
93	BD3	
94	BD4	***
95	BD5	"
96	BD6	11
97	BD7	***
98	BD8	11
99	BD9	Branch on 8 in Distributor 9

COL.	41	42	43 4447	48	49, 50	51	5255	56	57 5861	62	6372
	E-PO-FI	ળ−७ <b>ೱ</b>	LOCATION		RATION CODE		DATA ADDRESS	TAG	INSTRUCTION ADDRESS	TAG	REMARKS
				${\tt B}$	LR		FWA		LWA		
				B	LA		FWA		LWA		
				R	EG	*	FWA		LWA		X = ALPHABETIC CHAR.
				E	QU	S	YMBL		ANY		
			1	S	YN	S	YMBL		ANY		
				Α	LF	X	××××			-	
				B	OP		1				·
			i	Н	ED	#					# = HEADING CHAR.
			1	P	AT				1		
				R	EL		$\Delta_{ exttt{D}}$		$\Delta_{C}$		
				R	BR		FWA		LWA		
				R	EQ	S	YMBL		NNNN		

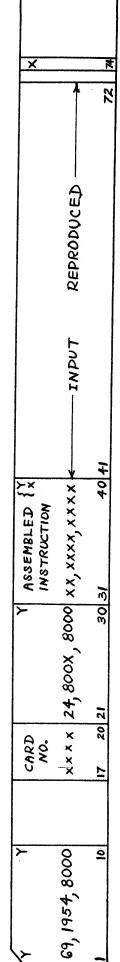
Summary of Pseudo Operations



Instructions, Data and ALF



800X Instructions



Availability Table:  $00 \le \alpha \le 49$ 

1-		
\hat{\}	****	77 80
<b>&gt;</b>	(XXXXXXXX 00) d+1000, d+1450 XXXXXXXXX 00, d+1500, d+1950 XXXXXX	12 02
Y	×××××××××××××××××××××××××××××××××××××××	
Υ Υ	00,4+1000,4+1450	41 50 51
<b>&gt;</b>	× × × × × × × × × × × × × × × × × × ×	31 40
Y	00, d, d+450 xxxxxxxxx 00, d+500, d+950 xx.	21 30 31
\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	XXXXXXXXX	11 20
X	٥٥, ط ع+450	Of I

DESCRIPTION OF OUTPUT CARDS

# 407 Control Panel

The 407 control panel shown in Figure 9 will detect the following situations in the assembled output:

### Note #

- 1 Instruction \* with blank L following an instruction with D and I both not blank.
- 2 Instruction with L not blank following an instruction with either D or I blank
- 3 Instruction with blank L not preceded by an instruction.
- 4 Non-instruction following an instruction with D or I blank.

Notes (1) and (2) always imply a coding error while (3) and (4) are warning signals to point out potential coding errors.

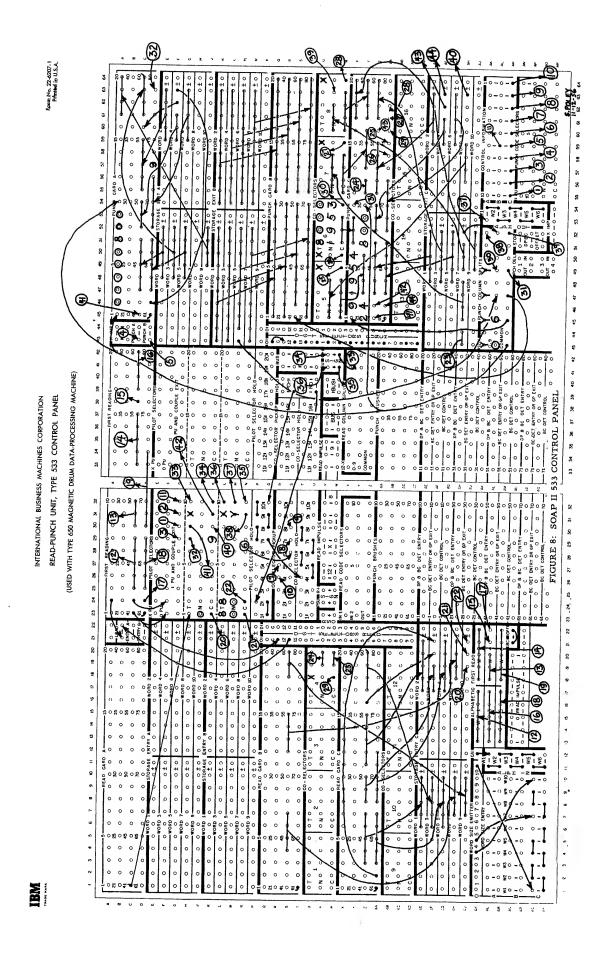
If the assembled L, OP, D or I fields of an instruction have been left blank, columns 75-78, respectively, will be x (11) punched. Correspondingly, the phrase

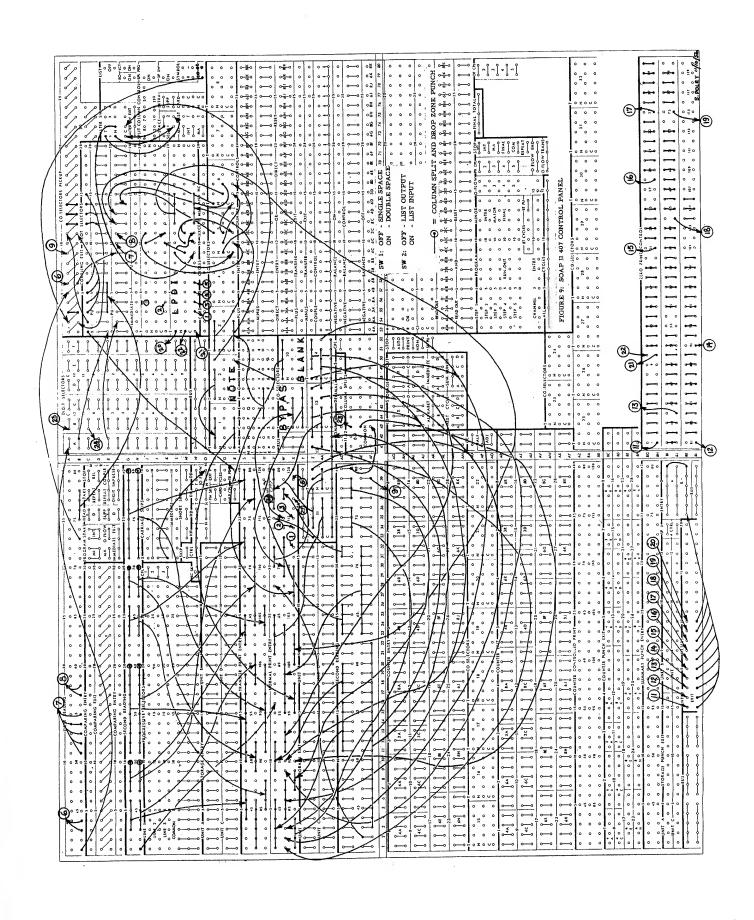
BLANK L, OP, D or I

will print to the right of the absolute instruction.

If an EQU or SYN card has been ignored due to an undefined I address or a packed symbol table, column 79 will contain a 9 punch causing the word 'BYPAS' to print to the right of the Remarks field.

<sup>\*</sup>Instruction → instruction or data.





# PART II: PROGRAM ANALYSIS

#### Introduction

The following is a description of the internal operation of SOAP II. An attempt has been made to present this description in a logical order beginning with allocation of memory followed by an explanation of the tables governing the assembly and outline of the various subroutines.

The symbolic assembly of SOAP II included at the end of this report was arrived at in the following manner:

- 1. SOAP II was coded symbolically for assembly by SOAP I.
- 2. Deck (1) was assembled by SOAP I, giving SOAP II in SOAP I form.
- 3. Deck (1) was translated into SOAP II symbolic form using the SOAP I to SOAP II Translator (N.Y.S.C.C. #334).
- 4. The symbolic SOAP II form of SOAP II (3) was assembled by Deck (2) giving SOAP II in SOAP II form.

The program is listed in logical order as opposed to the order in which it was assembled.

### Memory Allocation

Figure 1 is a "snapshot" of the drum showing the location of the various tables used by the assembly program and the input-output and program blocks.

# Table Descriptions

# Symbol Table: 0000-0399

Every symbol processed by the assembly program is stored in the symbol table. An attempt is first made to store a symbol in location L computed as follows:

Let S = XXXXXXXXXXX be the ten digit numerical representation of a five alphanumerical character symbol. Calculate

# 

Then L is taken to be the integral part of .4(ZZZ). Thus  $000 \le L_0 \le 399$ . It may be seen that all of the digits in S contribute to the formation of L0. Thus different symbols will tend to give rise to different L0's, but the problem of duplicity, i.e., different symbols having the same L0, must be accounted for. If a symbol cannot be stored in L0 (because another symbol has "gotten

	1951 1977 1990 1992	7
	PROGRAM	
	1164	
1050	1097 SYMBOLIC OP TABLE	
	REGION TABLE 2 2 ENTRIES 2	1049
0060	MARĐORA	6660
0800	OPTIMIZING TABLE	6680
0090	AVAILABILITY TABLE	0799
0400	EQUIVALENCE TABLE	0299
0000	SYMBOL TABLE	0399

FIGURE 1: MEMORY ALLOCATION

there first"), an attempt is made to store it in (L<sub>0</sub> + 1). If this too fails, we try L<sub>0</sub> + 2, L<sub>0</sub> + 3, ....., L<sub>0</sub>+ K where K is (hopefully) small.

#### Equivalence Table: 0400-0599

Whenever a symbol is stored in the symbol table, its four digit equivalent is stored in the equivalence table in the following manner:

Let L be the location in the symbol table containing the symbol. Let

$$\frac{L}{2} = Q + \frac{R}{2}$$

Then the equivalent is stored in the data address or instruction address part of (0400 + Q) according as R = 0 or 1.

#### Availability Table: 0600-0799

The availability table "remembers" drum locations used by the program being assembled and those still remaining. Each of the 200 words contains indicator digits (0  $\rightarrow$  unavailable, 1  $\rightarrow$  available) for ten dynamically equivalent drum locations. If these digits are numbered from left to right, P = 0, 1, 2, ..., 9, then the digit corresponding to a drum location L may be found as follows:

Let L = 
$$X_1 X_2 X_3 X_4$$
. Let  $2L = Y_1 Y_2 Y_3 Y_4$ .  
Then
$$A = 0600 + Y_1 + 2(Y_3 Y_4)$$

$$P = Y_2$$

where A is the availability table location whose P<sup>th</sup> digit corresponds to L.

Example: 
$$L = 1238$$
  
 $2L = 2476$   
 $A = 0600 + 2 + 2(76) = 0754$   
 $P = 4$ 

Conversely, given A and P, one may compute L as follows:

Let 
$$\frac{A-0600}{4} = Q + \frac{R}{4}$$
  
Then  $L = 500R + 50P + Q$   
Example:  $A = 0754$ ,  $P = 4$   
 $\frac{0754 - 0600}{4} = \frac{154}{4} = 38 + \frac{2}{4}$ 

$$L = 500 \times 2 + 50 \times 4 + 38 = 1238$$

Optimizing Table: 0800-0899

Information required to optimize 650 instructions is stored in the optimizing table. Location (0800 + XX) contains the entry for numerical op code XX where  $00 \le XX \le 99$ . Thus the entry for RAL (65) is in 0865.

As there is no one rule by which all 650 instructions may be optimized, operations having the same or similar optimizing rules have been grouped as shown in Figure 2. The general optimizing rules for these groups are:

999: I = D + 8 where 8 = f(D)998: I = L + 8 where  $8 \neq f(D)$ 898: I = L + 8 where 8 = f(D), non-shift 988: I = L + 8 shift other than SRD 888: I = L + 8 SRD

Optimizing table entries have the following format:

$$\begin{pmatrix}
E & \overline{O} & E & \overline{O} \\
X & X & XX & XX & OABC
\end{pmatrix} \qquad
\begin{cases}
\overline{O} = ODD \\
E = EVEN
\end{cases}$$

where ABC is the group code given in Figure 2. Thus the entry for RAL is (3305040999).

GROUP CODE	OPERATIONS
999	UFA, AUP, SUP, DIV, ALO, SLO, AML, SML, MPY, STL, STU, SDA, SIA, STD, FAD, FSB, FDV, FAM, FSM, FMP, RAU, RSU, DVR, RAL, RSL, RAM, RSM, LDD, LIB, LDI, SIB, STI, TLU, RD1, WR1, RC1, RD2, WR2, RC2, RD3, WR3, RC3, RCD, PCH, RPY
998	NOP, HLT, RTC, RTN, RTA, WTN, WTA, NTS, BIN, SET, NZA, BMA, NZB, BMB, NZC, BMC, NZU, NZE, BMI, BOV, NEF, RWD, WTM, BST, BDO, BD1,, BD9, SDS, RDS, WDS
898	AXA, SXA, AXB, SXB, AXC, SXC, RAA, RSA, RAB, RSB, RAC, RSC
988	SRT, SLT, SCT
888	SRD

Figure 2: 650 Operation Groups

Pseudo Operation Entries: 1001-1012

The twelve pseudo operations have been (arbitrarily) numbered as follows:

#	Pseudo Operation
$\frac{1}{1}$	BOP
2	BLR
3	BLA
4	REG
5	
6	EQU
	SYN
7	ALF
8	PAT
9	HED
10	REL
11	REQ
12	RBR

The first instruction of a pseudo operation is in location (1000 + #). Thus the REL routine begins in 1010.

#### Region Table: 1021-1049

Origins of the twenty-six possible regions are stored in the region table. The instruction address part of location (0960 + XX) contains the first word address (FWA) of the region having XX as the numerical representation of its alphabetic designation. Thus the origin of region F is in 1026 ( = 0960 + 66).

The region table is initialized to (800000000). Processing of an REG card with FWA = YYYY converts the appropriate entry to (900000YYYY).

#### Symbolic Op Table: 1050-1164 except 1098-1099 and 1148-1149

All pseudo operation codes and symbolic 650 operation codes are stored in the symbolic op table. The table is in ascending order on the six high digits of each entry representing the numerical representation of the alphanumerical op code.

The table entry for RAL, for example, is (7961730065) while that for REL is (7965731010). Thus the I address part of pseudo operation entries is the location of the first instruction of the pseudo routine while the two low order digits of 650 symbolic op code entries are the corresponding numerical op.

#### Type Entries: 1990-1992

The first instruction for types "blank", 1 and 2 is in location 1990, 1991, 1992 respectively.

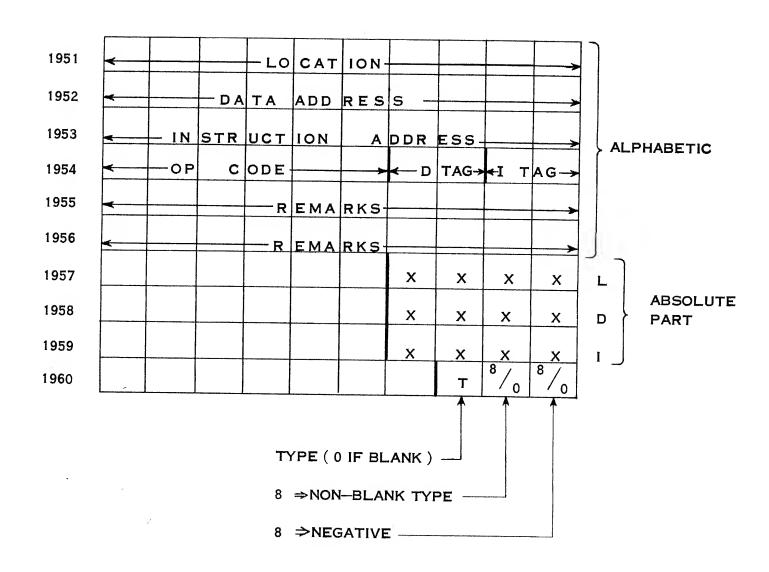


FIGURE 3: INPUT BLOCK: 1951 - 1960

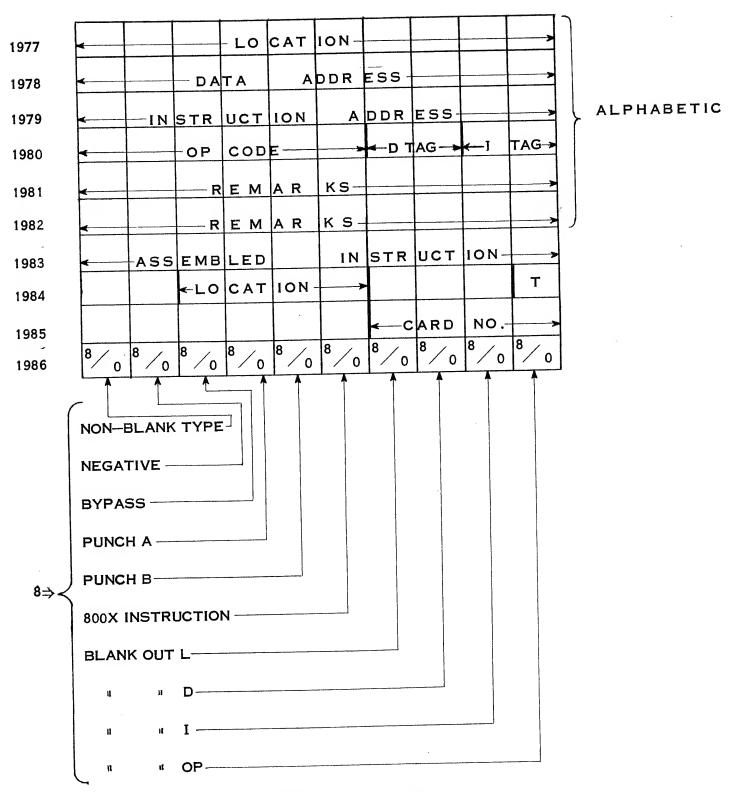


FIGURE 4: OUTPUT BLOCK: 1977 - 1986

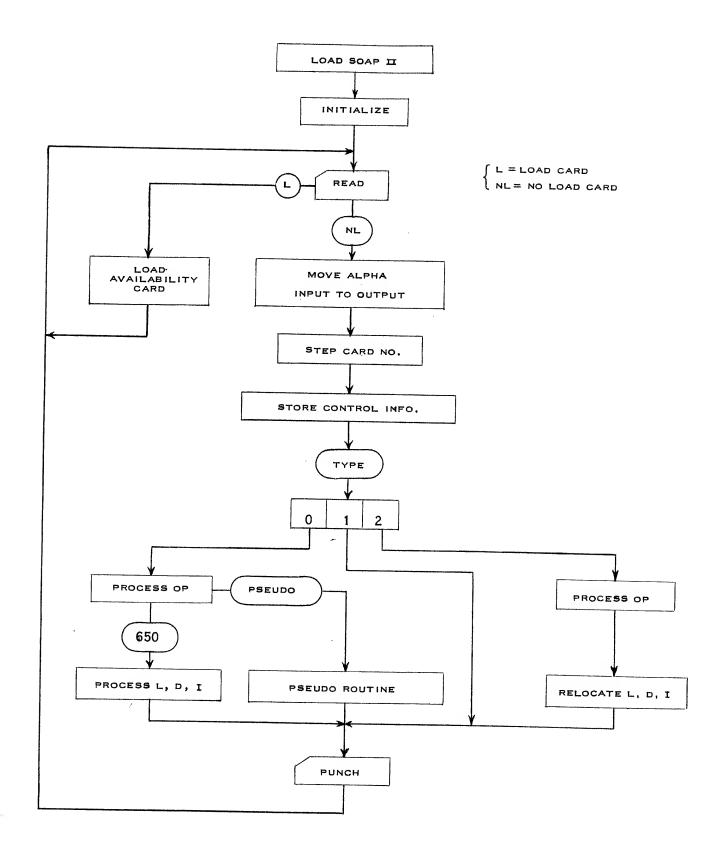
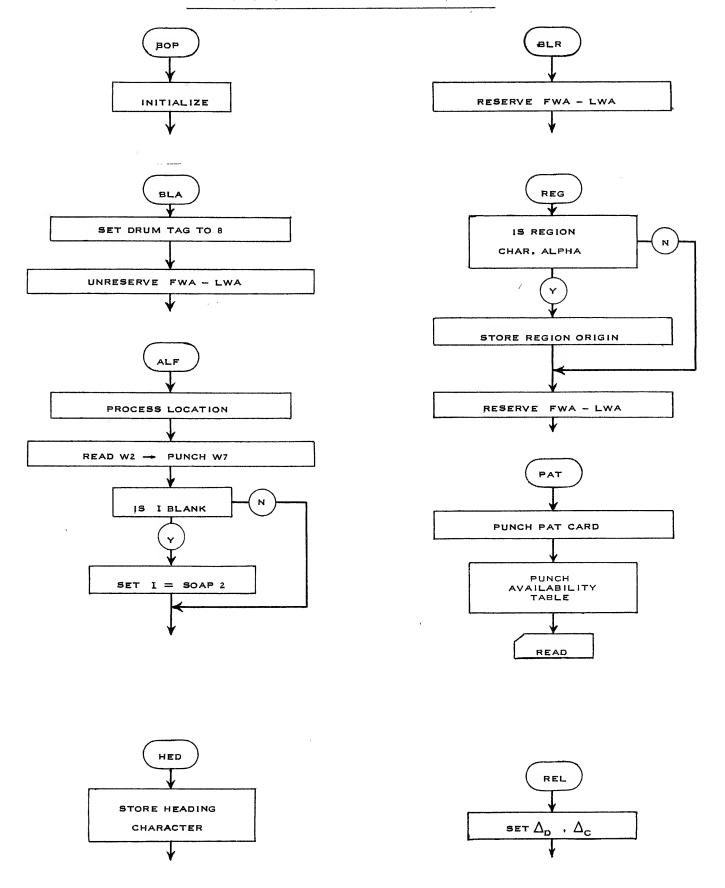
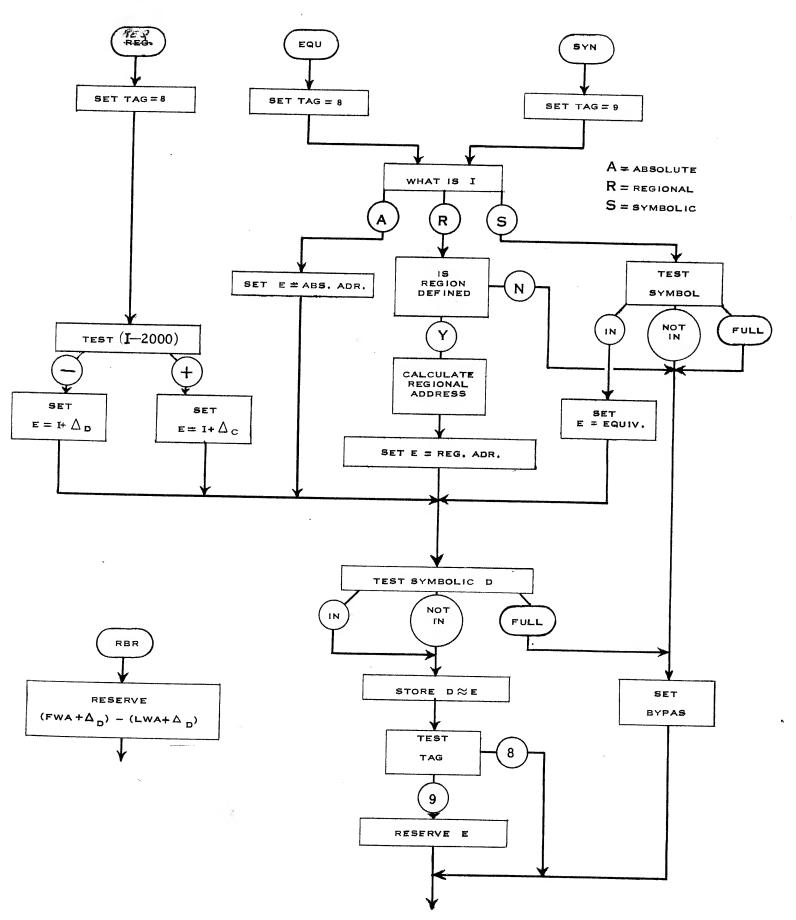


FIGURE 5: BASIC FLOW CHART

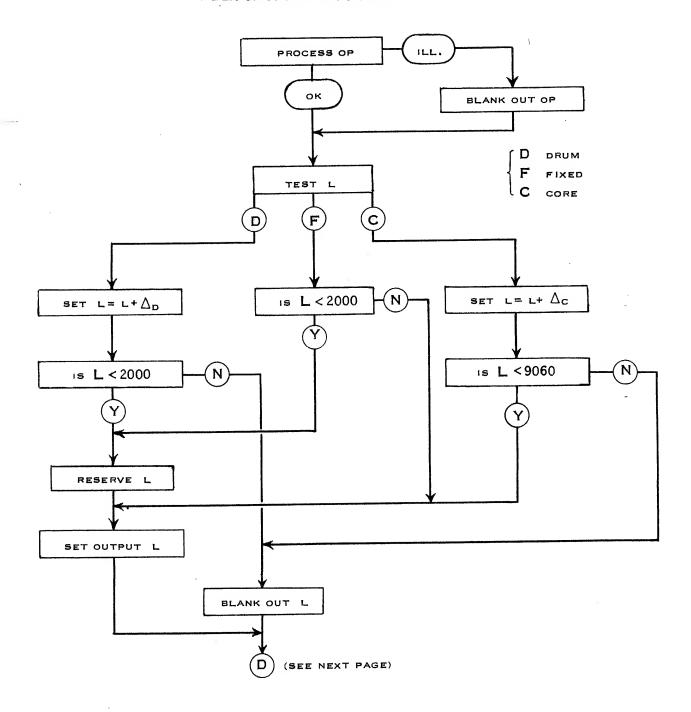
#### PSEUDO-OPERATION FLOW CHARTS



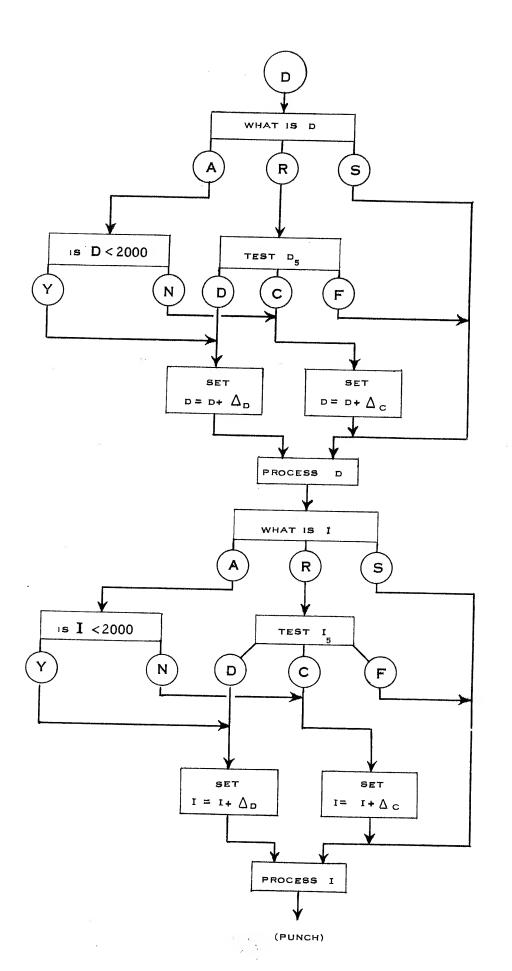
NOTE: EXCEPT FOR PAT, THESE ROUTINES TERMINATE AT "PUNCH"



#### RELOCATABLE ROUTINE



L MAY BE 1) |F NNNN | FIXED DRUM OR CORE
2) | NNNN | RELOCATABLE DRUM OR CORE



(D<sub>5</sub> = SYMBOLIZER
PART OF D)

#### Subroutines

SOAP II contains twenty-one subroutines. The following is a description of each subroutine giving

- A. Functional description
- B. Entry conditions and entry point
- C. Exit conditions (where applicable)
- D. Flow chart (where applicable)
- 1. Store K in all locations from FWA to LWA

Entry: UA (K) Go to SUBR1
LA(00, FWA, LWA)
DIST (exit)

2. Reserve or unreserve N sequential drum locations beginning at FWA.

Entry: UA (00,0000, FWA) Go to  $\frac{\text{SUB2R}}{\text{SUB2U}}$  - Reserve DIST (exit)

3. Process op code and exit to K or (K + 1).

Entry: DIST (00, 0000, K) Go to SUBR3

- Exit: K: 1. Legal 650 op; numerical op stored in punch word 7 and optimizing table entry stored in "OPTIM".
  - 2. Illegal symbolic op; "OPTIM" set to (5505050999) and punch control word set to blank out assembled op.

K + 1: Pseudo op (routine begins in 10XX)
 UA (00, 0000, 10XX)
 LA (irrelevant)

4. Determine whether address is absolute, regional or symbolic and accordingly exit to K, (K + 1), or (K + 2).

Entry: UA (alpha. address) Go to SUBR4
LA (clear)
DIST (00, 0000, K)

Exit: K: Absolute

K + 1: Regional }

K + 2: Symbolic 

UA (alpha.address)

LA (clear)

Note: If address is symbolic, it is automatically headed and stored in "HSYMB." Upon exiting, the UA also contains the headed symbol.

5. Test absolute address A and exit to K, (K + 1), (K + 2) or (K + 3).

Entry: UA (irrelevant)

Go to SUBR5

LA (00, 0000, A) DIST (00,0000, K)

K: A < 2000 Exit:

K + 1: A = 800X

K + 2:  $9000 \le A \le 9059$ 

UA (clear) LA (00,0000, A)

K + 3: Other

6. Test symbol and exit to K, K + 1, or K + 2.

Entry: UA (symbol)

Go to SUBR6

LA (clear)

DIST (00,0000, K)

Exit: K: In table

Equivalent in I address

part of "EQUIV"

LA (00, L\*, 0000)
"LSYMB" (00, L\*, 0000)

K+1: Not in table

K+2 Not in table and symbol table packed.

Note: L\* = symbol table location containing symbol on exit K or location in symbol table where symbol may be stored on exit (K + 1).

7. Store symbol in symbol table  $L^*$  location and equivalent E in equivalence table.

Entry: UA (symbol)

Go to SUBR7

LA (00, L\*, E)

DIST (exit)

8. Obtain equivalent E of symbol stored in symbol table location L\*.

Entry: UA (clear)

Go to SUBR8

LA (XX, L\*, XXXX)

DIST (exit)

Exit: UA (clear)

LA (00, L\*, 0000)

"EQUIV" (00, 0000, E)

9. Calculate regional address A and exit to K or (K + 1).

Entry: UA (alpha. regional address) Go to SUBRULA (clear)
DIST (00, 00  $\alpha \beta$ , K);  $\alpha \beta = \begin{cases} 90 - L \\ 88 - D \\ 89 - I \end{cases}$ 

Exit: K: UA (clear)

LA (00,0000, A)

K + 1: Region undefined or A negative.

10. Set 'PUNCH A" 8 in punch control word and execute punch instruction.

Entry: UA (exit) Go to SUB10
LA (clear)

11. Calculate optimum dynamic address and find and reserve "best" drum location L; exit to K or (K + 1).

Entry: UA (clear)

LA (00,00  $\alpha \beta$ , K):  $\alpha \beta = \begin{cases} 90 - L \\ 88 - D \\ 89 - I \end{cases}$ Go to SUB11

Exit: K: UA (clear)

LA (00,0000, L)

K+1: Drum packed

12. Set 8 in punch control word to blank out assembled location.

Entry: UA (exit) Go to SUB12
LA (clear)

13. Calculate optimum dynamic address XX.

Entry: DIST (00, 000 **\beta**, K) Go to <u>SUB13</u>

$$\beta = \begin{cases} 8-D \\ 9-I \end{cases}$$

Exit: K: UA (clear)

LA (00, 0000, 00XX)  $00 \le XX \le 49$ 

14. Punch availability table.

Entry: DIST (exit)

Go to SUB14

15. Head symbol.

Entry: UA

UA (symbol) LA (clear) DIST (exit)

Go to SUB15

-----

Exit:

UA (headed symbol)

LA (clear)

16. Initialize assembly.

Entry: DIST (exit)

Go to SUB16

17. Calculate drum equivalent E of 800X

Entry: UA (exit)

Go to  $\begin{cases} \underline{SB17D} - D \text{ equivalent} \\ \underline{SB17I} - I \text{ equivalent} \end{cases}$ 

Exit: UA (clear)

LA (00, 0000, E)

18. Index address A giving  $A^*$ .

Entry: UA (irrelevant)

LA (00, 0000, A) DIST (00,  $00 \propto \beta$ , K)

 $\alpha = \begin{cases} 8-\text{Drum} \\ 9-\text{Core} \end{cases}, \quad \beta = \begin{cases} 8-\text{Drum} \\ 9-\text{Drum} \end{cases}$ 

Exit:

UA (irrelevant) LA (00, 0000, A\*)

19. Process location.

Entry: DIST (exit)

Go to PROCL

Go to INDEX

20. Process data address.

Entry: DIST (exit)

Go to PROCD

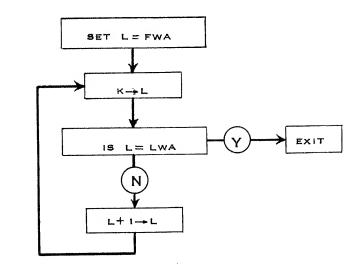
21. Process Instruction address.

Entry:

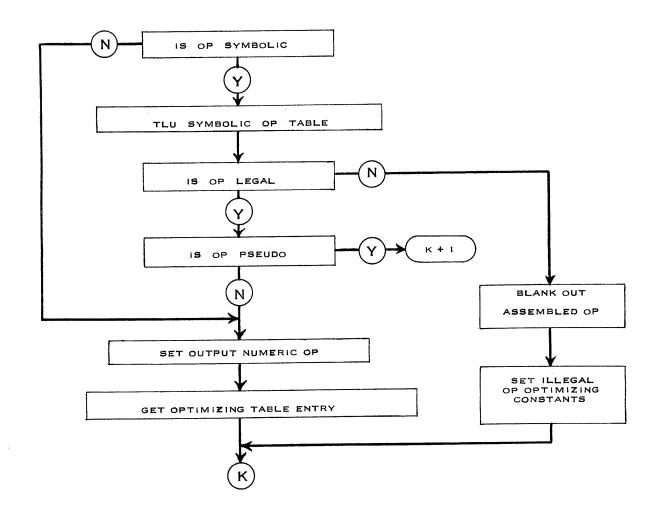
DIST (exit)

Go to PROCI

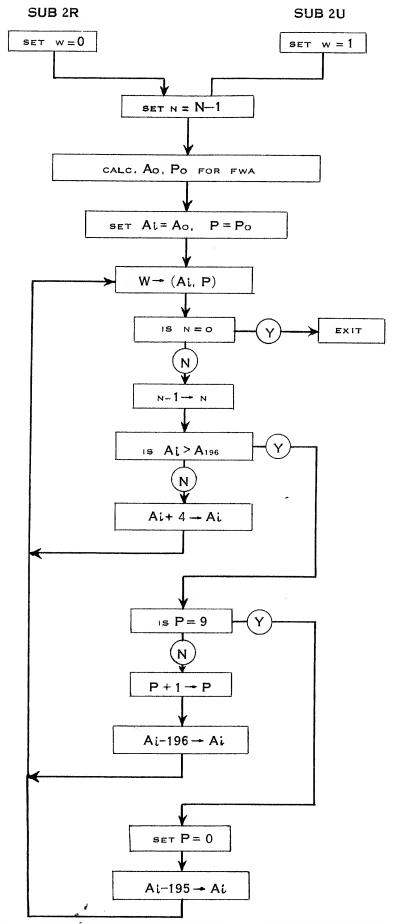
#### SUBROUTINE FLOW CHARTS



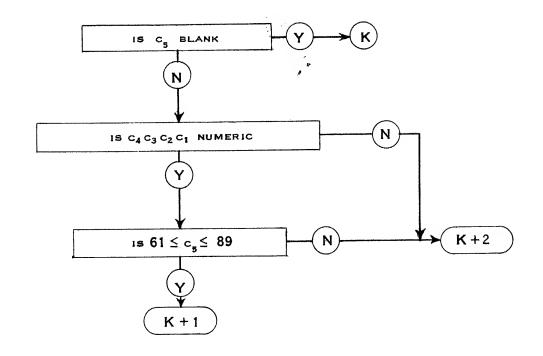
SUBROUTINE 1: STORE K IN FWA - LWA



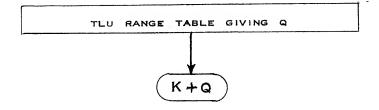
51



SUBROUTINE 2: RESERVE OR UNRESERVE N WORDS STARTING AT FWA

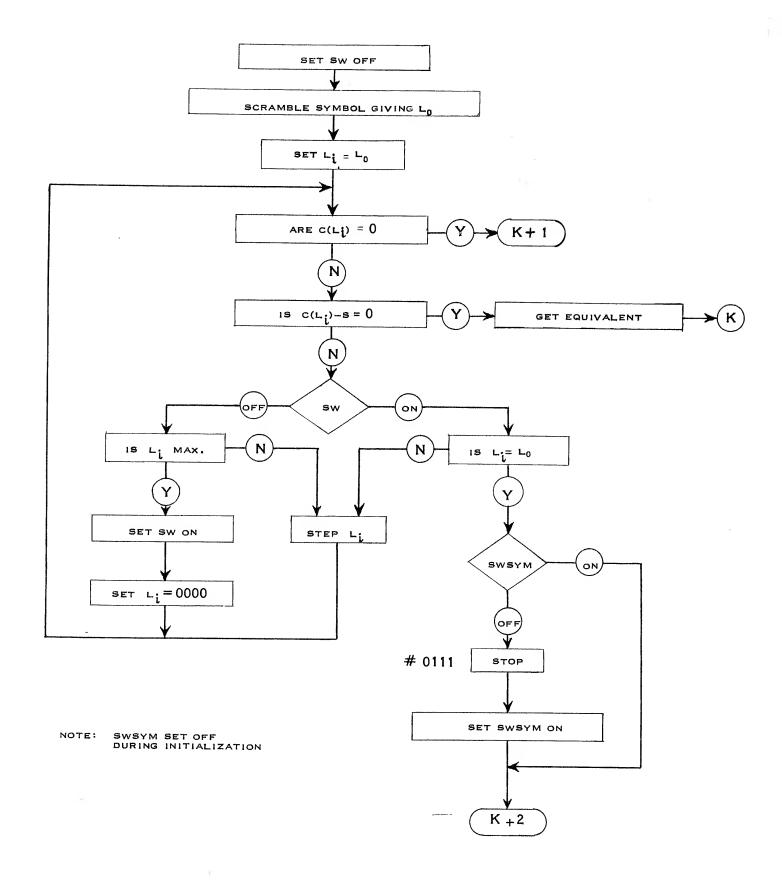


SUBROUTINE 4: ADDRESS TEST

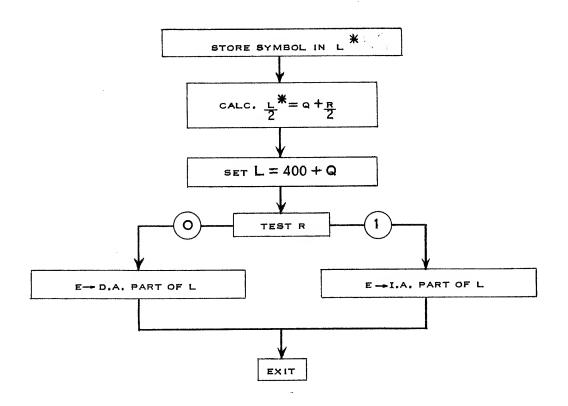


Q	RANGE		
0	0000≤ A ≤ 1999		
1	$\begin{cases} 8000 \le A \le 8003 \\ 8005 \le A \le 8007 \end{cases}$		
	<sup>1</sup> 8005 ≤ A ≤ 8007		
2	$9000 \le A \le 9059$		
3	OTHER		

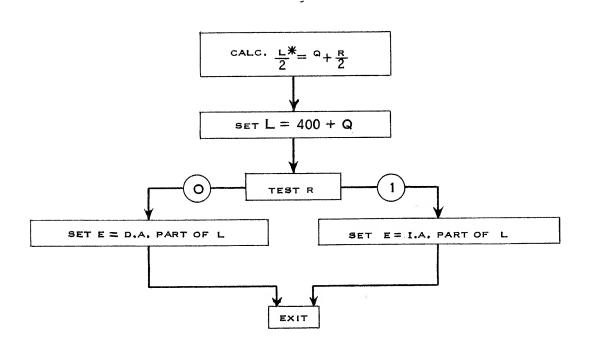
SUBROUTINE 5: ADDRESS RANGE TEST



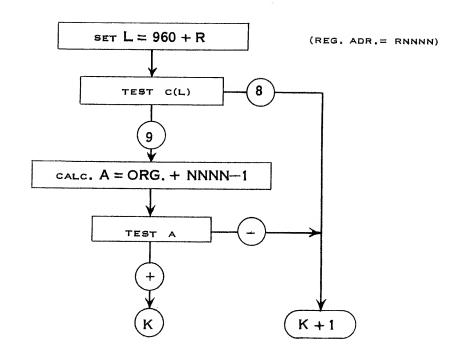
SUBROUTINE 6: SYMBOL TEST



SUBROUTINE 7: STORE SYMBOL AND EQUIVALENT



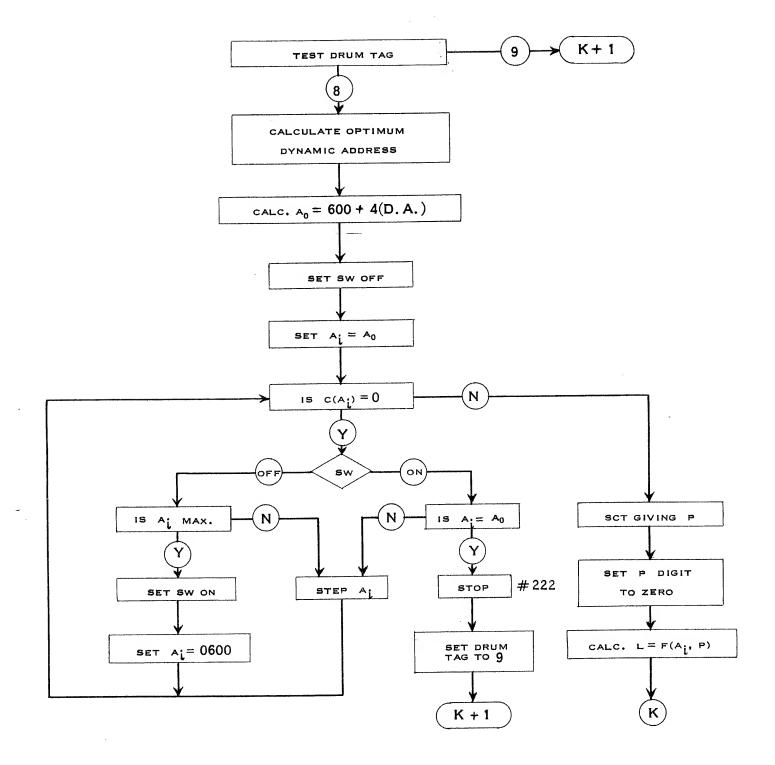
SUBROUTINE 8: OBTAIN SYMBOLIC EQUIVALENT



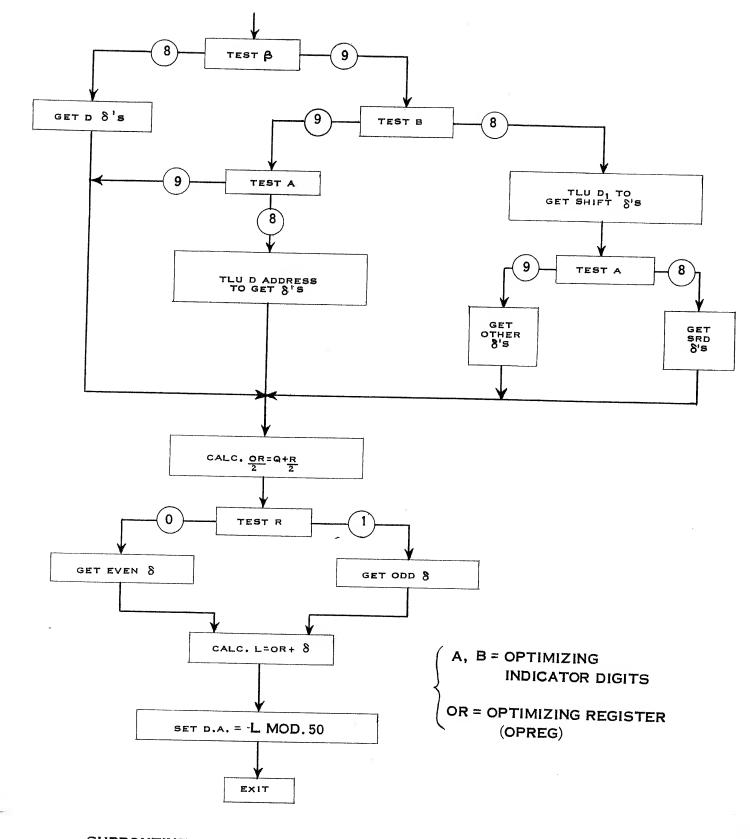
SUBROUTINE 9: CALCULATE REGIONAL ADDRESS

SUBROUTINE 10: (SEE PROGRAM LISTING)

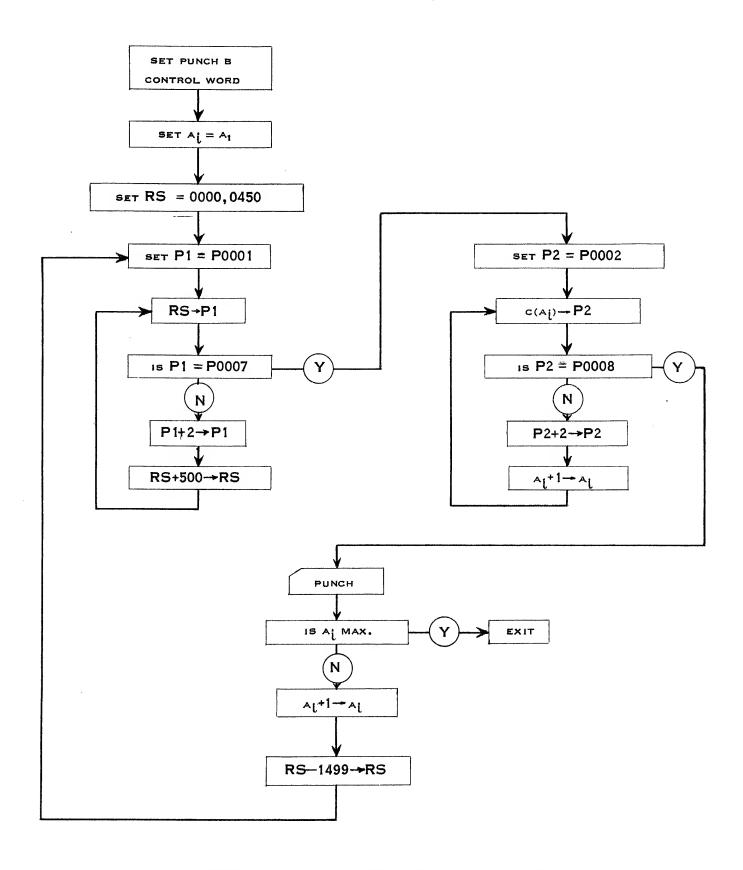
SUBROUTINE 12: (SEE PROGRAM LISTING)



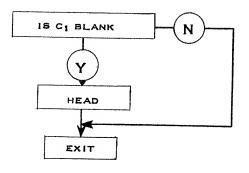
SUBROUTINE 11: FIND AND RESERVE BEST DRUM LOCATION



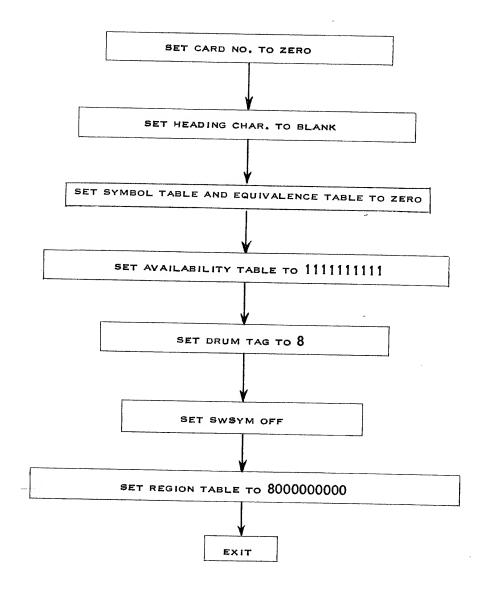
SUBROUTINE 13: CALCULATE OPTIMUM DYNAMIC ADDRESS



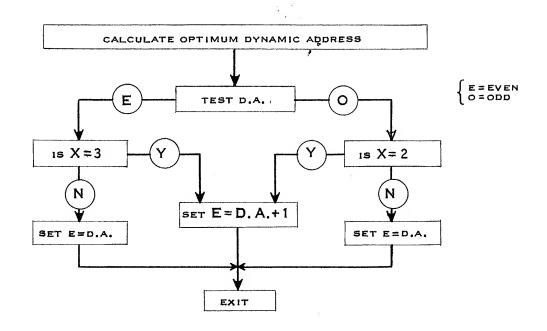
SUBROUTINE 14: PUNCH AVAILABILITY TABLE



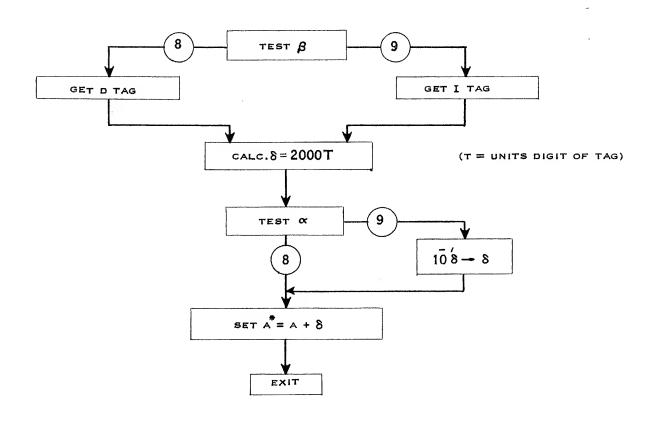
SUBROUTINE 15; HEADING



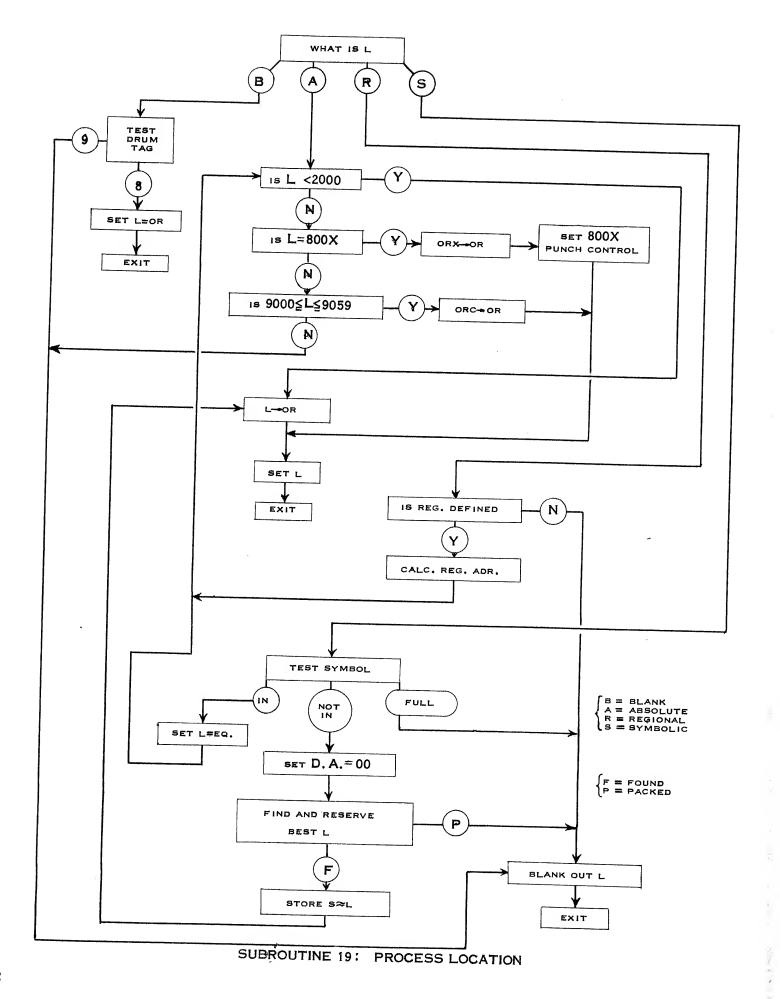
SUBPOUTINE 16: INITIALIZATION

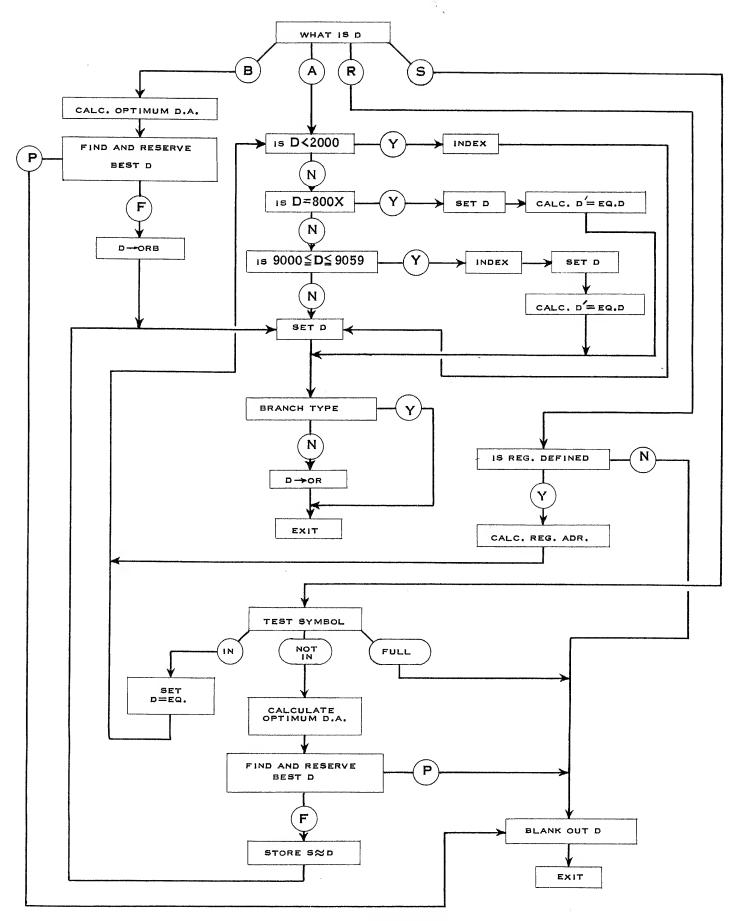


SUBROUTINE 17: CALCULATE 800X DRUM EQUIVALENT

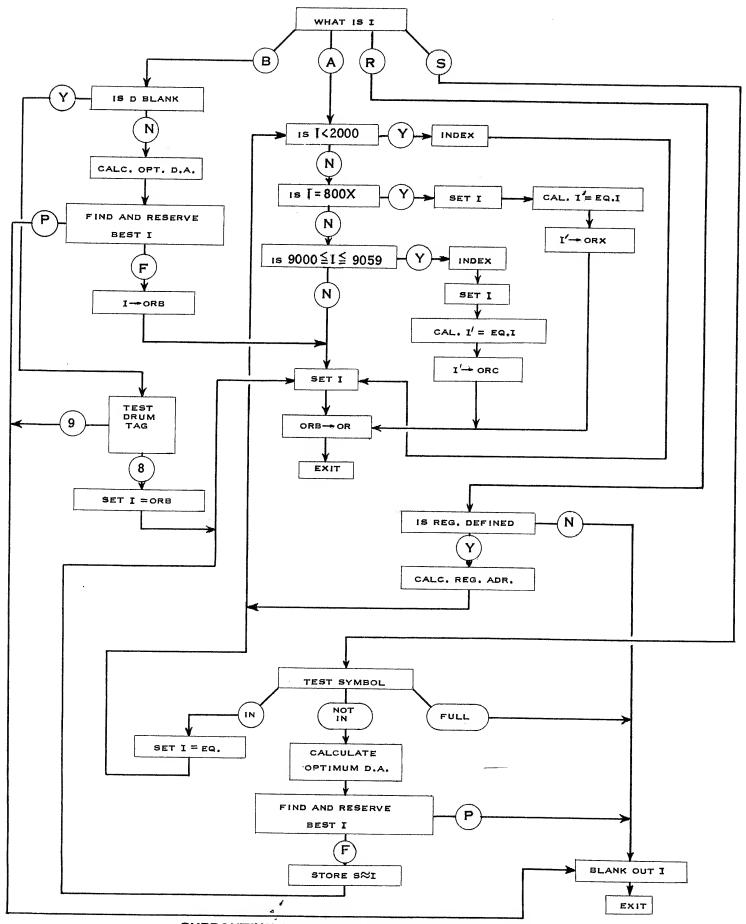


SUBROUTINE 18: INDEX ADDRESS





SUBROUTINE 20: PROCESS DATA ADDRESS



SUBROUTINE 21: PROCESS INSTRUCTION ADDRESS

SOAP 2

## SYMBOLIC OPTIMAL ASSEMBLY PROG

FOR JHE

IBM 650

#### DATA PROCESSING SYSTEM

	REG	50000	0399	SYMBOL TAB				
	REG	E0400	0599	EQUIV TABL				
	REG REG	A0600	0799	AVAIL TABL NUM OP TAB				
	BLR	N0800 1000	0899 1000	ENTRY				
	BLR	1001	1012	PSEUDO ENT				
	BLR	1001	1049	REG TABLE				
	REG	C1050	1164	SYM OP TAB				
	BLA	1098	1099	3111 01 170				
	BLA	1148	1149				•	
	BLR	1900	1900	MANUAL PAT				
	SYN	READC	1950					
	BLR	1951	1962	READ AREA				
	REG	T1990	1992	TYPE				
	REG	P1977	1986 -	PUNCH AREA				
	BLR	1998	1999					
	EQU	XXXX1	1961					
	EQU	XXXX2	1962					
	BLR	1563	1570	TYPE2				
	BLA	1565	1565					
	BLA	1569	1569					
	REG	B0900	0901	SUB 2				
	REG	H0902	0904	TYPE 2				
	REG	M0905	0907					
	REG	Q0908	0909					
	REG	R1200	1207	SUB 5				
	REG	U1250	1259	SUB 13				
	REG	V0910	0912	EQU SYN				
	REG	X0913	0914	TYPE O				
	REG	Y0915	0916	EQU SYN				
	REG	Z0917	0919					
	REG BLR	W0920 0923	0922 0968	MULTIPLE R				
	REG	I 1300	1307	SUB 13				
	1120	11500	1001	300 13				
		CONTROL	PROGRAM					
1000	LDD	READC	SUB16	ENTRY	1000	69	1950	1353
		1000	1000	mmam	1050	~ ^	1000	1000
READC	RCD	1999	1998	READ CARD TRANSFER	1950 19 <b>9</b> 8	70 69	1999 19 <b>5</b> 1	1998 1354
1998	LDD	1951		ALPHABETIC	1354	24	1977	0980
	STD LDD	P0001 1952		INPUT	0980	69	1952	1355
	STD	P0002		TO	1355	24	1978	0981
				· •		- ·	<b>.</b> - · •	
								65

	LDD STDD STDD STAL STAL STAL STU	1953 P0003 1954 P0004 1955 P0005 1956 P0006 P0009 11XXX P0009 1960 0002 P0008		STEP CARD NUMBER STORE CONTROL	0981 1356 0982 1357 0983 1208 0984 1209 0985 0989 0997 0988 1015	69 249 249 249 249 25 120 60 30	1953 1979 1954 1980 1955 1981 1956 1982 1985 0992 1985 1960	1356 0982 1357 0983 1208 0984 1209 0985 0989 0997 0988 1015
	AUP	TORG		INFO	09 <b>71</b> 098 <b>7</b>	21 10	1984 099 <b>0</b>	098 <b>7</b> 0995
8003	ST <b>L</b> NOP	P0 <b>01</b> 0 0000	800 <b>3</b> 9999	TRANSFER TO TYPE	0995 8003	20 00	198 <b>6</b> 0000	8003 9999
PUNCH	PCH	P0001	READC	PUNCH READ	1350	71	1977	1950
SETCC	RAU	READC	SUB10	SUDO EXIT	1400	60	1950	1405
1900	LDD	READC	SUB14	MANUAL PAT	1900	69	1950	1403
T0001	LDD	X	SUBR3	TYPE 0	1990	69	0993	0996
X0001	L D D L D D	PUNCH	PROCL PROCD PROCI	650 COMMAND OR CONSTANTS	0913 1016 1019	69 69 69	1016 1019 1350	09 <b>6</b> 9 09 <b>7</b> 2 1 <b>4</b> 53
X0002	NOP	0000	8003	PSEUDO OP	0914	00	0000	8003
T0002	RAU	READC	SUB10	TYPE 1 COM	1991	60	1950	1405
X	00	0000	X0001		0993	00	0000	0913
		RELOCATE	ROUTINE					
T0003 Q0002 Q0001	HED LDD NOP RAL SLT NZU RAU	T Q 0000 1951 0002 1957	SUBR3 ILLOP	PROCESS OP ILLEGAL OP IS L FIXED FIXED L	1992 0909 0908 1455 1211	69 00 65 35 44	1195 0000 1951 0002 1165	0996 1013 1455 1211 1166
FC SR SETL	SUP BMI AUP RAL RAL SLT LDD SDA	8001 1957 XXXX1 0004 P0008 P0008	FC RES SETL SETL	FIXED L DRUM CORE FIXED DRUM FIXED CORE SET L	1165 1261 1169 1172 0973 1450 1311 1171	60 11 46 10 65 65 35 69 22	1957 1014 1172 8001 1957 1961 0004 1984 1984	1261 1169 0973 0979 1311 1311 1171 1187 1237

REL RES RC	RAUPI BMUPI BMUPU BATTO RALO BALO RAU	1957 2000I DDIFF 2000I XXXX1 SR 1957 CDIFF 9060	RC BL RES SUB2R BL SETL SUB12	DRUM CORE  RELOCATE DRUM ADDR  RESERVE L RELOCATE CORE	1166 1361 1219 1222 1179 1182 0979 1214 1173 1411 1269 0977 1180 1183	60 11 46 10 46 10 21 96 55 16 46 15 60	1957 1014 1222 0975 1182 1014 1961 1450 1957 1264 1272 1180 8001 1237	1361 1219 1173 1179 1183 0979 1214 1503 1411 1269 0977 1183 1311 0991
<b></b>	REG REG	J0944 G0941	0944 0941	D TYPE 2				
PROD  H0001  H0002  1563 1564 1566 CDD  H0003	DDDUDLOITPLLLOP GG	PROI EXITX 1952 H 1958 2000I 1564 0008 1500 CD1FF 1958 1958 0000 J0960 F0955	SUBR4  1563  8003 CDD CDD J0001 J0001 G0003  0960 0955	SET D EXIT WHAT IS D RELOCATE D OR C C D OR F C D F SYMBOLIC I	1237 1193 0999 1407 0902 1263 1319 0903 1221 1563 1564 1566 1369 0904	69409566956695500 69566955500	1190 1196 1952 1210 1958 1014 1564 0008 0974 1264 0975 1958 0000	1193 0999 1407 1213 1263 1363 1369 1369 1369 0944 0943
M0001 M0002 1567 1568 1570 CDI M0003	L DD DD L SR L D L S R L D L S R L D L L L L D L L L L D P L L L D	PUNCH EXITX 1953 M 1959 2000I 1568 0008 1504 CDIFF DDIFF 1959 1959 0000	SUBR4 1567 8003 CDI CDI J0001 J0001 F0003	SET I EXIT WHAT IS I RELOCATE D OR C C D OR F C D F SYMBOLIC	1190 1553 1099 1457 0905 1313 1419 0906 1175 1567 1568 1570 1469 0907	69 24 60 65 16 46 30 65 65 55 10	1350 1196 1953 1260 1959 1014 1568 0008 0978 1264 0975 1959 0000	1553 1099 1457 1213 1313 1419 1567 1175 8003 1469 1469 0960 0960 0957
Q 9060 H 1500	00 00 00 00	0000 0000 0000 0000	Q0001 9060 H0001 1500	CONSTANTS	1195 1272 1210 0974	00 00 00	0000 0000 000 <b>0</b> 0000	0908 9060 0902 1500

M 1504 XXXX1	00 00 01 HED	0000 0000 0000	M0001 1504 XXXX1	ERAS <b>E</b> AB <b>L</b> E	1260 0978 1961	00 00 01	0000 0000 0000	
		BOP ROL	JTINE					
1001	LDD RAU	READC	SUB16 SUB10	INITIALIZE PUNCH	1001 1404	<b>6</b> 9 60	1404 1950	1353 1405
		BLR ROU	TINE					
1002 BLR	RAL SLO AUP LDD	1959 1958 8001 SETCC	BLR SUB2R	RESERVE FWA TO LWA	1002 1363 1413	65 16 10	1959 1958 8001	1363 1413 1271
		RBR ROU			1271	69	1400	1503
1012	D 444		1 114					•
1012	RAU ALO	DDIFF 1959	BLR		1012 1229	60 15	09 <b>75</b> 1959	1229 13 <b>63</b>
		BLA ROU	TINE					
1003	LDD STD RAL SLO AUP LDD	H8XXX DRUMT 1959 1958 8001 SETCC	SUB 2U	SET DRUM TAG TO 8 UNRESERVE FWA TO LWA	1003 1309 1215 1463 1513 1321	69 24 65 16 10	1406 1212 1959 1958 8001 1400	1309 1215 1463 1513 1321 1603
		REG ROUT	INE					
1004	HED RAL SRT SLO BMI ALO BMI SLT ALO	R 1952 0008 90XXX 29XXX 1002 0004 ST 1958	1002	IS REGION CHARACTER ALPHABETIC STORE REGION ORIGIN	1004 1507 1225 1233 0986 1243 1197 1557 1265	65 30 16 46 15 46 35 10	1952 0008 1178 0986 1189 1002 0004 1310 1958	1507 1225 1233 1002 1243 1197 1557 1265 1613
8002	AUP S <b>TU</b>	H9XXX 9999	8002 1002		1613 8002	10 21	1216 9999	8002 1002
st	STU HED	1021	1002	CONSTANTS	1310	21	1021	1002
	1	EQU REQ	AND SYN R	OUTINE				
1005 1006 BOTH	HED LDD LDD STD	Z H8XXX H9XXX TAG	вотн	EQU SYN	1005 1006 1359	69	1216	1359 1359 1315

Z0001 Z0002 Y0001 Y0002 Z0003 W0001 W0002 W0003	RAD LAD LAD STAL RAD RAL RAL	1953 Z 1959 Y E P0010 W EQUIV P0010 P0010	SUBR4 Y0001 SUBR9 TD BP SUBR6 Y0001 BP BP	WHAT IS I'  ABSOLUTE REGIONAL  REG ERROR SYMBOLIC S DEFINED S UNDEFIND S TAB FULL	1315 1607 0917 0918 0915 0916 0919 0920 0921	60 65 65 65 65 65 65 65 65 65 65	1953 1360 1959 1371 1519 1986 1372 1223 1986	1607 1213 0915 1174 1322 1191 1275 0915 1191
V0003 V0001 V0002 SD	RAU LDD LDD RAL ALO ALO LDD	1952 V P0010 E E TT	SUB15 SUBR6 BP SD SD SUBR7	HEAD D AND EQUATE TO E S TAB FULL	1322 1657 1410 0912 0910 0911 1273	60 69 69 65 15 15	1952 1410 1713 1986 1519 1519 0976	1657 1663 1275 1191 1273 1273 1279
TT	LDD BDO RAU LDD	TAG SETCC E SETCC	SUB 2R	TEST TAG RESERVE E	09 <b>76</b> 1365 0970 1323	69 90 60 69	1262 1400 1519 1400	1365 0970 1323 1503
1011 D CD	LDD STD RAL SLO BMI ALO ALO	H8XXX TAG 1959 2000I D CDIFF DDIFF 2000I	CD CD Y0001	REQ ROUTINE IS I DRUM O COR	1011 1409 1415 1763 1569 1373 1422 1619	69 24 65 16 46 15 15	1406 1262 1959 1014 1422 1264 0975 1014	1409 1415 1763 1569 1373 1619 1619 0915
Z Y W V 88	00 00 00 00	0000 0089 0000 0000 8800	Z0001 Y0001 W0001 V0001 0000	CONSTANTS	1360 1371 1372 1713 1500	00 00 00 00	0000 0089 0000 0000 8800	0917 0915 0920 0910 0000
BP~	ALO STL	88 P0 <b>01</b> 0	PUNCH	BY PASS	1191 1505	15 20	1500 1986	1505 1350
TAG E	01 01 HED	0000	TAG E	ERASEABLE	1262 1519	01 01	0000	12 <b>6</b> 2 1 <b>51</b> 9
		ALF ROUT	INE					
1007	LDD LDD STD RAL NZE LDD STD	1952 P0007 1953 PUNCH S0AP2 P0003	PUNCH	PROCES LOC  REPLACE BLANK I BY SOAP2	1007 1460 1555 1186 1707 1461 1017	69 69 24 65 45 45 69	1460 1952 1983 1953 1350 1314 1979	0969 1555 1186 1707 1461 1017 1350

SOAP2	ALF	SOAP2	SOAP2	CONSTANT	1314	82	7661	7792	
		PAT ROUT	INE						
1008	RAU LDD	READC	SUB10 SUB14		1008 1511	60 69	1511 1950	1405 1403	
		HED ROUT	INE						
1009	RAL SRT STL	1952 0008 0000H	SETCC	STORE HEADING CHARACTER	1009 1757 1325	65 30 20	1952 0008 1329	1757 1325 1400	
REL ROUTINE									
1010 SDD	HED RAL NZE RAL STL	M 1952 1958 DDIFF	SDD SDD	SET DRUM AND CORE	1010 1807 1510 1561	65 45 65 20	1952 1510 1958 0975	1807 1561 1561 1228	
SCD	RAL NZE RAL STL	1953 1959 CDI <b>F</b> F	SCD SCD SETCC	DELTAS	1228 1857 1560 1611	65 45 65 20	1953 1560 1959 1264	1857 1611 1611 1400	
D <b>DI</b> FF C <b>DI</b> FF	01 01 HED	0000 0000	DDIFF CDIFF	ERASEABLE	0975 1264	01 01	0000	09 <b>7</b> 5 1 <b>264</b>	
		LOAD AVA	ILABILITY	TABLE					
1999	HED RAL SRT SLT ALO ALO	L 1951 0004 0004 8002 8002		CALC LOC W2	1999 1605 1465 1375 1283 1241	15 15	1951 0004 0004 8002 8002 0994	1605 1465 1375 1283 1241 1149	
8003 8002 TUP	AUP LDD STD SUP NZU AUP ALO	C2 9995 9994 TW TWP2 1DXXX	8003 8002 TUP READC 8003	LOAD TABLE	1149 8003 8002 1247 1655 1459	10 69 24 11 44 10	1352 9995 9994 1550 1459 1312 1020	8003 8002 1247 1655 1950 1167 8003	
C1 C2 TW TWP2	STD LDD LDD LDD HED	A0001 1952 1958 1960	TUP 8002 8002 8002	CONSTANTS	0994 1352 1550 1312	24 69 69 69	0600 1952 1958 1960	1247 8002 8002 8002	

## OPTIMIZING TABLE

N0001 N0002	00 00	0404 0404	0998	NOP		0800	00	0404	0998
N0002	33	2322	0998 0999	HLT		0801	00	0404	0998
N0004	00	0505	0998	UFA RTC		0802	33	2322	0999
N0005	00	0505	0998	RTN		0803 0804	00	0505	0998
N0006	00	0505	0998	RTA		0805	00	0505 0505	0998
-N0007	00	0505	0998	WTN		0806	00 00	0505	0998
N0008	00	0505	0998	WTA		0807	00	0505	0998
N0009	33	1212	0999	LIB		0808	33	1212	0998 0999
N0010	33	0202	0999	LDI		0809	33	0202	0999
N0011	33	0504	0999	AUP		0810	33	0504	0999
N0012	33	0504	0999	SUP		0811	33	0504	0999
N0013	99	9999	9999	NOT	USED	0812	99	9999	9999
N0014	99	9999	9999		USED	0813	99	9999	9999
N0015	33	1110	0999	DIV		0814	33	1110	0999
N0016	33	0504	0999	ALO		0815	33	0504	0999
N0017	33	0504	0999	SLO		0816	33	0504	0999
N0018	33	0504	0999	AML		0817	33	0504	0999
N0019	33	0504	0999	SML		0818	33	0504	0999
N0020	33	2120	0999	MPY		0819	33	2120	0999
N0021	54	0303	0999	STL		0820	54	0303	0999
N0022	45	0303	0999	STU		0821	45	0303	0999
N0023	34	0303	0999	SDA		0822	34	0303	0999
N0024	34	0303	0999	SIA		0823	34	0303	0999
N0025	33	0303	0999	STD		0824	33	0303	0999
N0026	44	0505	0998	NTS		0825	44	0505	0998
N0027	00	0505	0998	BIN		0826	00	0505	0998
N0028	00	0505	0998	SET		0827	00	0505	0998
N0029	33	1212	0999	SIB		0828	33	1212	0999
N0030	33	0202	0999	STI		0829	33	0202	0999
N0031	00	0000	0988	SRT		0830	00	0000	0988
N0032	00	0000	0888	SRD		0831	00	0000	0888
N0033	33	2726	0999	FAD		0832	33	2726	0999
N0034	33	2 <b>72</b> 6	0999	FSB		0833	33	2726	0999
N0035	33	0000	0999	FDV		0834	33	0000	0999
N0036	00	0000	0988	SLT		0835	00	0000	0988
N0037	00	0000	0988	SCT		0836	00	0000	0988
N0038	33	2726	0999	FAM		0837	33	2726	0999
N0039	33	2726	0999	FSM		0838	33	2726	0999
N0040	33	0000	0999	FMP		0839	33	0000	0999
N0041 N0042	33	0404	0998	NZA		0840	33	0404	0998
	33	0404	0998	BMA		0841	33	0404	0998
N0043 N0044	33	0404	0998	NZB		0842	33	0404	0998
N0044	33	0404	0998	ВМВ		0843	33	0404	0998
N0045	34	0405	0998	NZU		0844	34	0405	0998
N0046	43	0504	0998	NZE		0845	43	0504	0998
N0047	33 33	0404	0998	BMI		0846	33	0404	0998
N0049		0505	0998	BOV		0847	33	0505	0998
N0050	33 33	0404 0404	0998 0998	NZC		0848	33	0404	0998
N0051	00	0000	0998	BMC AXA		0849	33	0404	0998
NO052	00	0000	0898	SXA		0850 08 <b>51</b>	00	0000	0898
N0053	00	0000	0898	AXB		0852	00 00	0000	08 <b>9</b> 8 08 <b>98</b>
	• •	3330	0070	AVD		0002	UU	0000	0070

N0055 N00556 N00556 N000589 N000661 N000663 N0006667 N000667 N000667 N000774 N000775 N000778 N000778 N000881 N000889 N000889 N000889 N000997 N000997	040000033993333333000000000000000000000	00555500 00505500 00505500 00505500 00505500 00505500 00505500 00505500 005000 0050 00500	0998 898 09998 09998 099999999999999999	DD  SEE  SEFDMTTCCUUTTTR  SSRRNND11112223333YAAABBUSSSCC0123456  SRRNNDDRRRRRLRWRRRWRRRRRRRRRRBBBBBBBBBBBBBBBB	0853 0854 0855 08556 08557 08556 08559 08661 088666 088667 088667 08877 08877 08877 08877 08877 08877 08888 08888 08888 08888 08888 08888 08888 08888 08889 08889 088999 0889 0889 0889 0889 0889 0889 0889 0889 0889 0889 0889 0899	040000033993333330000000000000000000000	05555000449990444430000000000000000666660005555555 00050000099910000000000000000	88888888888888888888888888888888888888
N0097 N0098 N0099 N0100	33 33 33 44	0505 0505 0505 0505	0998 0998 0998 0998	B <b>D6</b> B <b>D7</b> B <b>D</b> 8 B <b>D</b> 9	089 <b>6</b> 089 <b>7</b> 0898 0899	33 33 33 44	0505 0505	0998 09 <b>98</b>
110 100		SYMBOLIC		<i>50</i> 7	0077	<del></del>	0505	0998
C0001 C0002 C0003 C0004 C0005	61 61 61 61	7366 7376 7473 8477 8761	1007 0015 0017 0010 0050	ALF ALO AML AUP AXA	1050 1051 1052 1053 1054	61 61 61 61	7366 7376 7473 8477 8761	1007 0015 0017 0010 0050

C0006	61	8 <b>76</b> 2	0052	AXB	1055	61	8762	0052
C0007	61	8763	0058	AXC	1056	61	8763	0058
C0008	62	6476	0090	BDO	1057	62	6476	0090
C0009	62	6491	0091	BD1	1058	62	6491	0091
C0010	62	6492	0092	BD2	1059	62	6492	0092
C0011	62	6493	0093	B <b>D</b> 3	1060	62	6493	0093
C0012	62	6494		BD4		62	6494	0094
			0094		1061			
C0013	62	6495	0095	BD5	1062	<b>6</b> 2	6495	0095
C0014	62	6496	0096	B <b>D6</b>	1063	62	6496	0096
C0015	62	6497	0097	B <b>D7</b>	1064	62	6497	0097
C0016	62	<b>649</b> 8	0098	BD8	1065	62	6498	0098
C0017	62	6499	0099	B <b>D</b> 9	1066	62	6499	0099
C0018	62	6975	0026	BIN	1067	62	6975	0026
C0019	62	7361	1003	BLA	1068	62	7361	1003
C0020	62	73 <b>7</b> 9	1002	BLR	1069	62	7379	1002
C0021	62	7461	0041	BMA	1070	62	7461	0041
C0022	62	7462	0043	ВМВ	1071	62	7462	0043
C0023	62	7463	0049	BMC	1072	62	7463	0049
C0024	62	7469	0046	BMI	1073	62	7469	0046
C0025	62	7677	1001	BOP -	1074	62	7677	1001
C0026	62	7685	0047	BOV	1075	62	<b>76</b> 85	0047
C0027	62	8283	0057	BST	1076	62	8283	0057
C0028	64	6985	0014	DIV	1077	64	6985	0014
C0029	64						8579	
		8 <b>57</b> 9	0064	DVR	1078	64		0064
C0030	65	7884	1005	EQU	1079	65	7884	1005
C0031	6 <b>6</b>	6 <b>164</b>	0032	FAD	1080	66	6164	0032
C0032	66	6174	0037	FAM	1081	66	6174	0037
C0033	66	6485	0034	FDV	1082	66	6485	0034
C0034	66	7477	0039	FMP	1083	66	7477	0039
C0035	66	8262	0033	FSB	1084	66	8262	0033
C0036							8274	
	66	8274	0038	FSM	1085	66		0038
C0037	68	6 <b>56</b> 4	1009	HED	1086	<b>6</b> 8	6564	1009
C0038	68	7383	0001	HLT	1087	<b>6</b> 8	7383	0001
C0039	73	6464	0069	L.DD	1088	73	6464	0069
C0040	73	6469	0009	LDI	1089	73	6469	0009
C0041	73	6962	0008	LIB	1090	73	6962	0008
C0042	74	7788	0019	MPY	1091	74	7788	0019
C0043	75	6566	0054	NEF	1092	75	6566	0054
C0044	75	7 <b>67</b> 7	0000	NOP	1093	75	7677	0000
C0045	75	8382	0025	NTS	1094	<b>7</b> 5	8382	0025
C0046	75	8961	0040	NZA	1095	<b>7</b> 5	8961	0040
C0047	75	8962	0042	NZB	1096	75	8962	0042
C0048	75	8963	0048	NZC	1097	75	8963	0048
C0051	75	89 <b>6</b> 5	0045	NZE	1100	75	8965	0045
C0052	75	8984	0044	NZU	1101	75	8984	0044
C0053	77	6183	1008	PAT	1102	77	6183	1008
C0054	77	6368	0071	PCH	1103	7 <b>7</b>	6368	0071
C0055	79	6161	0080	RAA	1104	79	6161	0080
C0056	79	6162	0082	RAB	1105	79	6162	0082
C0057	79	6163	0088	RAC	1106	79	6163	0088
						79	6173	
C0058	79	6173	0065	RAL	1107			0065
C0059	79	6174	0067	RAM	1108	79	6174	0067
C0060	79	6184	0060	RAU	1109	<b>7</b> 9	6184	0060
C0061	79	<b>627</b> 9	1012	RBR	1110	<b>7</b> 9	6279	1012
C0062	79	6364	0070	RCD	1111	79	6364	0070

C0063 C0064 C0065 C0066 C0067 C0068 C0069 C0070 C0071 C0072 C0073 C0075 C0076 C0077 C0078 C0081 C0082 C0083 C0084 C0085 C0088 C0087 C0088 C0089 C0099 C0099 C0099 C0099 C0097 C0101 C0102 C0103 C0106 C0107 C0108 C0107 C0108 C0109 C0101	79999999999999999999999999999999999999	63939321233738812338441354431231233232123373881233344413543123374434934712354412123	0075 0078 0078 00778 00776 00776 10011 00081 000	CCC3S123GLQYABCLMUACNDTASTABOTLUPABCNUAS123RRRRRRRRRRRRRRRRRRSSSSSSSSSTUWWWW	1112 1113 1114 11116 11117 11120 11223 11224 11226 11221 11221 11221 11231 11334 11336 11339 11441 11451 1151 11511 11511 11511 11511 11511 11511 11511 11511 11511 11511 1151	799999999999992222222222223466669999999999	123212321232123212321232123212321232123	00778 00778 0007788 000778 000778 000778 000778 000778 000778 000778 000778 0007788 000778 000778 000778 000778 000778 000778 000778 000778 0007788 000778 000778 000778 000778 000778 000778 000778 000778 0007788 000778 000778 000778 000778 000778 000778 000778 000778 0007788 000778 000778 000778 000778 000778 000778 000778 000778 0007788 000778 000778 000778 000778 000778 000778 000778 000778 0007788 000778 000778 000778 000778 000778 000778 000778 000778 0007788 0007
C0107	84	6 <b>66</b> 1	0002	UFA	11 <u>56</u>	84	6661	0002
C0108	86	6482	008 <b>7</b>	WDS	1157	86	6482	0087
C0109	86	<b>7</b> 991	0071	WR1	1158	86	7991	0071

SUB 1 STORE K IN FWA TO LWA

SUBR1  8003 NEXT	HEDU STUDAT SDAU SULP NAU STUP STUDE	1 EXITX XXXX1 FWA 0004 XXXX2 FWA XXXX1 9999 XXXX2 8001 1DXXX 9998	8003 NEXT EXITX 8003 NEXT	STORE EXIT STORE K SET FWA LWA ADDRESSES STORE K END OF LOOP TEST STEP	1600 1199 1364 1170 1220 1181 1515 1421 8003 1402 1267 1471 1379	24 21 69 22 35 22 60 15 20 11 44 10 10	1196 1961 1217 1217 0004 1962 1217 1961 9999 1962 1471 8001 1020	1199 1364 1170 1220 1181 1515 1421 8003 1402 1267 1196 1379 8003
XXXX2	01 HED	0000	XXXX2	ERASEABLE	1962	01	0000	1962
	1120	SUB 2 RE	ESERVE UNR	ESERVE				
	HED	2						
SUB2R	STD	EXITY Z	DU	RESERVE	1503	24	1813	1316
SUB2U	LDD STD	EXITY	RU	ENTRY UNRESERVE	1316 1603	69 24	1769 1813	1472 1366
	LDD	1 I	RU	ENTRY	1366	69	1565	1472
RU	STD STL	W N		SET W STORE N	1472	24	1246	1299
	SLO	8001		CALC AO	1299 15 <b>56</b>	20 16	1653 8 <b>001</b>	1556 1913
	AUP	8003		AND PO	1913	10	8003	1571
	SRT	0003			1571	30	0003	1479
	STU SUP	X 8001			1479	21 11	1284	1287 1245
	SLT	0001			128 <b>7</b> 1245	35	8001 0001	1451
	STU	P			1451	21	1606	1609
	SUP	8001			1609	11	8001	1367
	SLT AUP	0002 8 <b>00</b> 3			1367 1473	35 10	0002 8 <b>0</b> 03	1473 1281
	AUP	X			1281	10	1284	1239
	ALO	Р			1239	15	1606	1711
	SLT	0004			1711	35	0004	1621
	ALO STL	C1 B0002			1621 1761	15 20	1506 0901	1761 1554
	LDD	C2			1554	69	1188	1291
	SDA	SPR			1291	22	1231	1334
CH	AUP	C3	SU		1334	10	1337	1423
SU	STU AUP	SA C4	8003		1423 1604	21 10	1401 1558	1 <b>604</b> 8003
8003	RAL	9992	B0002		8003	65	9992	0901
B0002	SLT	0009	XX		0901	35	0009	1521
XX	STU	UH			1521	21	1176	1429
	SLT RAL	0001 8 <b>0</b> 02	•		1429 1185	3 <b>5</b> 65	0001 8002	1185 1293

SPR SA BOOO1	ARTP STL STL STAUP STAUP BMUP	W 0001 UH 0009 9983 N 1I N SA A197	SPR SA BOOO1 EXITY TP SU	IS N ZERO REDUCE N BY 1 IS AI IN LAST BLOCK OF	1293 1351 1907 1231 1401 0900 1308 1362 1669 1456 1705 1863 1266	10 30 10 30 20 65 45 16 20 60 11 46	1246 0001 1176 0009 9983 1653 1365 1653 1401 1358 1266 1719	1351 1907 1231 1401 0900 1308 1813 1669 1456 1705 1863 1317 1423
ΤP	RAL SLO NZE ALO LDO SDA STL RAU SUP	SPR C2 C5 C1 B0002 SPR SA C6	ZP SU	IS P 9  STEP P  REDUCE AI BY 196	1317 1235 1343 1296 1703 1509 1454 1184 1755	65 16 45 15 69 22 20 60 11	1231 1188 1296 1249 1506 0901 1231 1401 1408	1235 1343 1297 1703 1509 1454 1184 1755 1423
ZP	LDD STD LDD STD RAU SUP	C1 B0002 C7 SPR SA C8	SU	SET P TO ZERO REDUCE AI BY 195	1297 1559 1504 1661 1234 1805	69 24 69 24 60 11	1506 0901 1458 1231 1401 1508	1559 1504 1661 1234 1805 1423
C1 C2 C3 C4 11 A197 A201 C5 C6 C7 C8 Z W N B0002 SPR SA UH X	SLT SRTL STL STL STL STL SRO OO OO OO OO OO HED	0000 0009 A0001 0000 0000 A0197 A0201 0010 0196 0000 0195 0000 0000 0000 0000 0000	XX SA B0001 0001 B0001 B0001 SA 0000 SA 0000 W N B0002 SPR SA UH X	CONSTANTS	1506 1188 1337 1558 1565 1358 1719 1249 1408 1458 1508 1769 1246 1653 0901 1231 1401 1176 1284	35 30 20 45 00 20 30 00 00 01 01 01	0000 0009 0600 0000 0796 0800 0196 0000 0195 0000 0000 0000 0000 0000	1521 1401 0900 0001 0900 0900 1401 0000 1401 0000 1246 1653 0901 1231 1231 1401 1176 1284

SUB 3 PROCESS OP CODE

HED 3

SUBR3	STD	EXITX		STORE EXIT	0996	24	1196	1349
30017	RAL	1954		OTORE EXT.	1349	65	1954	1659
	SRT	0004		IS OP	1659	30	0004	1819
		0004	TILOD	BLANK	1819	45	1522	1013
	NZE	0001	ILLOP	IS OP	1522	35	0006	1387
	SLT	0006			1387	44	1341	1192
	NZU		NUM	NUMERIC				
	SRT	0002			1341	30	0002	1347
	STL	XXXX1		SEARCH	1347	20	1961	1414
	SLO	8001		SYMBOLIC	1414	16	8001	1671
	TLU	C0001		OP TABLE	1671	84	1050	1855
	ALO	1	8002		1855	15	1608	8002
8002	RAL	9997	NEXT	IS	8002	65	9997	1501
NEXT	SLO	XXXX1	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	SYM OP	1501	16	1961	1615
HEAT	SLT	0006		LEGAL	1615	35	0006	1529
	NZU	ILLOP			1529	44	1013	1384
				TEST FOR	1384	35	0001	1391
	SLT	0001		PSEUDO OP	1391	44	1295	1346
	NZU	PSEU				35	0001	1753
	SLT	0001	SNO	STORE NUM	1346			1236
SNO	STL	P0 <b>0</b> 07		650 OP	1753	20	1983	
	SRT	0004		GET OPTIM	1236	30	0004	1397
	ALO	3	8002	CONSTANTS	1397	15	1650	
8002	LDD	9996	NEX		8002	69	9996	1399
NEX	STD	OPTIM	EXITX		1399	24	1452	1196
,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	0.0							
NUM	SLO	4		FORM	1192	16	1345	1449
NOM	SLT	0002		NUMERIC	1449	35	0002	1905
				OP	1905	16	8002	1963
	SLO	8002		CODE	1963	30	0001	1869
	SRT	0001	C 11 D	CODE		15	8001	1753
	ALO	8001	SN <b>O</b>		1869	15	0001	1175
				*** *** *** ***	3070	4 5	1986	1441
ILLOP	RAL	P0 <b>01</b> 0		ILLEGAL OP	1013	65		
	ALO	5		SET 8	1441	15	1194	1499
	STL	P0010		FOR BLANK	1499	20	1986	1289
	LDD	6	NEX	OP	1289	69	1242	1399
PSEU	SLT	0003		PSEUDO .	1295	35	0003	1803
	ALO	EXITX		OP	1803	15	1196	1551
	ALO	11XXX	8002		1551	15	0992	8002
	74 60	*******	0002					
1	RAL	0000	NEXT	CONSTANTS	1608	65	0000	1501
1 2	10	0000	0000	20113111111	1700	10	0000	0000
2 '					1650	69	0800	1399
3	LDD	N0001	NEX		1345	90	9000	0000
4	90	9000	0000			00	0000	0008
5	00	0000	0008		1194		0505	
6	55	0505	0999		1242	55	0505	0999
	HED							
		SUB 4 WF	HAT IS ADDF	RESS				
	HED	4						
SUBR4	STD	EXITZ		STORE EXIT	1213	24	1416	1919
COUNT	STU	XXXX1		STORE ADDR	1919	21	1961	1464
	SRT	0008		IS C5	1464	30	8000	1333
	NZU	0000	EXITZ	BLANK	1333	44	1437	1416
	STU	XXXX2	L/11/	SAVE C5	1437	21	1962	1665
	310	^^^^						

LOOP AEX SYM	RAUOUPVTULOIOILPOOUDO RAUOUSLZALMIOILPOOUDO	8002 90XXX LOOP H1 0002 LOOP XXXX2 90XXX 29XXX SYM 11XXX XXXX1 EXITZ 8002 XXXX1	LOOP SYM  SYM  AEX 8001 SUB15	ARE LOW 4 CHARACTERS NUMERIC  LOW 4 NUM IS C5 ALPHABETIC  REGIONAL  HED SYMBOL STEP EXIT	1665 1523 1383 1286 1393 1396 1853 1658 1417 1433 13443 1447 1715 1721 0998 1765 1018	60070754566565056095	8002 1178 1286 1339 1396 0002 1286 1962 1178 1336 1189 0998 0999 1961 1416 8002 1961 1018 1771	1523 1386 1399 1399 1858 1443 1449 1449 1721 1706 1662 1625
	STU	HSYMB	AEX	SILI LATI	1425	21	1230	1715
H1 HSYMB	10 01 HED	0000	0000 HSYMB	ERASEABLE	1339 1230	10 01	0000	0000
-		SUB 5 TE	ST ABSOLU	TE ADDRESS				
SUBR5	HED STD STL SLT SLO	5 EXITZ XXXX1 0004 8002		STORE EXIT	1750 1969 1514 1475	24 20 35 16	1416 1961 0004 8002	1969 1514 1475 1483
8002 X	TLU ALO RAU SLT SRT AUP ALO	R0001 GET 9972 0006 0006 EXITZ XXXX1	8002 X	TLU RANGE TABLE	1483 1656 8002 1177 1491 1706 1821	84 15 60 35 30 10	1200 1709 9972 0006 0006 1416	1656 8002 1177 1491 1706 1821
GET R0001 R0002 R0003 R0004 R0005 R0006 R0007 R0008	SUP RAU 00 00 00 00 00 00 99 HED	8003 0000 1999 7999 8003 8004 8007 8999 9059	8001 X 0000 0003 0001 0003 0001 0003 0002 0003	RANGE TABLE	1815 1709 1200 1201 1202 1203 1204 1205 1206 1207	11 60 00 00 00 00 00 00 99	8003 0000 1999 7999 8003 8004 8007 8999 9059 9999	8001 1177 0000 0003 0001 0003 0001 0003 0002
		SUB 6 SY	MBOL TEST	ROUTINE				
SUBR6	HED STD STU	6 EXITZ S	;	STORE EXIT STORE SYMB	12 <b>75</b> 1320	24 21	1 <b>416</b> 1601	1320 1654

				Þ				
15 <b></b>	LDD STD MPY RAL SLT MPY SRT SLT ALO STL SLO	SWOF SW SC 8002 0003 CF 0001 0004 L1P1 LOP1 1DXXX		SET SWITCH' OFF SCRAMBLE SYMBOL GIVING LO SET TEST WORD	1654 1861 1764 1238 1547 1806 1330 1587 1597 1856	69 24 19 65 35 19 35 15 20	1758 1610 1517 8002 0003 1759 0001 0004 1850 1865	1861 1764 1238 1547 1806 1330 1587 1597 1856 1168
SLI 8001 A	STL STL RAL NZE SLO NZE	LO LI 9990 S SW	SLI 8001 A UND DEF	SAVE LO INIT LI IS CONT OF LI ZERO IS CONT OF LI SYMB	1525 1487 8001 1395 1098 1756	20 20 65 45 16 45	1903 1614 9990 1098 1601 1610	1487 8001 1395 1549 1756 1811
SW OFF	RAL SLO NZE ALO	LI LMAX LMP1	OFF MAX SLI	IS LI MAXIMUM STEP LI	1610 1270 1227 1280	65 16 45 15	1614 1573 1280 1533	1270 1227 1331 1487
ON MAX	SLO NZE ALO LDD	LOP1 SWON	SWSYM SLI	IS LI EQUA TO LO STEP LI SET	1800 1708 1412 1331	16 45 15 69	1903 1412 1865 1434	1708 1664 1487 1537
NAA	STD RAL	SW L1	SLI	SWITCH ON ZERO LI	1537 1714	24 65	1610 <sub>-</sub> 1467	1714 1487
UND	RAL ALO STL RAL SLO STL	EXITZ 1IXXX EXITZ LI L1 LSYMB	ASU	UNDEFINED STEP EXIT	1549 1871 1647 1370 1420 1921	65 15 20 65 16 20	1416 0992 1416 1614 1467 1575	1871 1647 1370 1420 1921 1278
DEF ASU	RAL LDD AUP	LI ASU S	SUBR8 EXITZ	DEFINED GET EQUIVALENT	1811 1470 1278	65 69 10	1614 1278 1601	1470 1381 1416
SWSYM SYMOF SYMON	HLT LDD STD RAL ALO	O111 SYMON SWSYM EXITZ 2IXXX	SYMOF SYMON 8002	S TAB FULL SET SW SYM ON	1664 1218 1224 1971 1572	01 69 24 65 15	0111 1971 1664 1416 1771	1218 1224 1971 15 <b>72</b> 8002
SWOF SWON SC CF L1 L1P1 LMAX LMP1 S	RAL RAL 000 RAL RAL RAL 01	LI 0100 0000 0000 0001 E0000 E0001 0000	OFF ON 1001 0004 A A A A S	CONSTANTS	1758 1434 1517 1759 1467 1850 1573 1533 1601 1610	65 65 10 00 65 65 65 65 01	1614 1614 0100 0000 0000 0001 0399 0400 0000	1270 1800 1001 0004 1395 1395 1395 1601 1610

	LO LI LSYMB	01 01 01 HED	0000 0000 0000	LO LI LSYMB		1903 1614 1575	01 01 01	0000 0000 00 <b>0</b> 0	1903 1614 1575
			SUB 7 ST	ORE SYMBOL	AND EQUIV				
	SUBR7	HED STD LDD	7 EXITZ SS		STORE EXIT	12 <b>79</b> 1520	2 <b>4</b> 69	1416 1623	1520 1226
	8001 A	SDA STU SUP STL SRT DIV SLT ALO LDD SDA LDD SDA	XXXX2 9988 8001 XXXX1 0004 21XXX 0004 C1 C2 SEL C3 SER	8001 A	STORE SYMBOL SAVE E CALCULATE LOCATION OF E AND STORE	1226 8001 1541 1599 1814 1625 1232 1493 1651 1808 1864 1620	22 21 11 20 30 14 35 15 69 22 69 22	1962 9988 8001 1961 0004 1771 0004 1446 1704 1911 1617 1673	8001 1541 1599 1814 1625 1232 1493 1651 1808 1864 1620 1276
	OBEE B SS C1	STL SLO ALO LDD NZU SLT STU LDD	OBEE 8001 XXXX1 9986 SER 0004 9987 E0001	OBEE B SEL A B	CONSTANTS	1276 1484 1591 1431 1389 1244 1623 1446	20 16 15 69 44 35 21	1431 8001 1961 9986 1673 0004 9987 0400	1484 1591 1431 1389 1244 1911 1541 1389
(	C2 C3 OBEE SEL SER	SDA SIA 01 01 01 HED	9985 9984 0000 0000 0000	EXITZ EXITZ OBEE SEL SER	ERASEABLE	1704 1617 1431 1911 1673	22 23 01 01 01	9985 9984 0000 0000	1416 1416 1431 1911 1673
			SUB 8 GI	VEN SYM LOC	GET EQUIV				
:	SUBR8	HED STD LDD SDA RAL DIV SLT	8 EXIT 8003 LSYMB 8001 2DXXX 0004	-	STORE EXIT	1381 1637 1294 1328 1285 1148	24 69 22 65 14	1534 8003 1575 8001 1288	1637 1294 1328 1285 1148
ı	8002 TR LH LR	ALD NZU RAM RAM SRT LDD SIA RAL	E1 9975 8001 8001 0004 8003 EQUIV	8002 TR LH LR LR	GET E RH GET E LH	1148 1809 8002 1378 1481 1282 1489 1439 1496 1326	35 69 44 67 67 39 35	0004 1462 9975 1481 8001 8001 0004 8003 1223 1575	1809 8002 1378 1282 1439 1439 1439 1496 1326
				1		<b>-</b>			

E1 EXIT EQUIV	LDD 01 01 HED	E0001 0000 0000	TR EXIT EQUIV	CONSTANT ERASEABLE	1462 1534 1223	69 01 01	0400 0000 0000	1378 1534 1223
	uen	SUB 9	CALCULATE RE	G ADDRESS				
SUBR9	HED STD SRT RAL SLT	9 EXITZ 0008 8003 0004		STORE EXIT	1174 1670 1539 1697	24 30 65 35	1416 0008 8003 0004	1670 1539 1697 1858
8002 NEX	ALO RALO SLT RAL SRT LDO	9991 ILL 0001 8002 0001 EXITZ	8002 NEX	IS REGION DEFINED	1858 8002 1445 1701 1908 1667 1723 1720	15 65 95 65 66 95 95	1512 9991 1649 0001 8002 0001 1416 1773 1376	8002 1445 1701 1908 1667 1723 1720 1675 1428
L D I ALL	BD5 ALO ALO ALO SLO BMI	D 1957 1958 1959 1IXXX ILL	I ALL ALL ALL EXITZ		1675 1376 1428 1562 1747	15 15 15 16 46	1957 1958 1959 0992 1649	1562 1562 1562 1562 1747 1416
ILL	RAL ALO	EXITZ 11XXX	8002	UNDEFINED OR ILLEG	1649 1622	65 15	1 <b>416</b> 0992	1 <b>622</b> 8002
С	RAL	0960	NEX	CONSTANT	1512	65	0960	1445
	HED	SUB 10	SET CC 8 AND	PUNCH				
SUB10	ALO ALO STL PCH	P0010 84TH P0010 P0001	8003		1405 1641 1699 1589	15 15 20 71	1986 1344 1986 1977	1641 1699 1589 8003
8 <b>4</b> TH	00	0800	0000	CONSTANT	1344	00	0800	0000
		SUB 11	FIND AND RES	ERVE BEST				
SUB11	HED LDD BDO STL BD6	O DRUMT EXIT DI	SEX	TEST DRUM TAG IS ADR L	1801 1965 1770 1478	69 90 20 96	1212 1770 1725 1531	1965 1820 1478 1633
DI	RAL	8003 F	SSW	L D OR I	1633 1531	65 69	8003 1634	1 <b>741</b> 1787
GDA	SDA SLT ALO	XXXX1 0004 8002	SUB13	FIND OPTIM DYNAM ADR	1787 1851 1762	22 35 15	1961 0004 8002	1964 1762 1672
SSW	ALO LDD STD	8002 OFF SW	SSW	SET SW OFF	1672 1741 1797	15 69 24	8002 1444 1584	1741 1797 1837

SAI 8001 TA SW SWOF	ALO STLO STLU STLU NAU RALO NALO NALO NALO	AlP1 BP1 1DXXX AO AI 9975 YES AI AMAX AMP1 AO	SAI 8001 TA SW SWOF MAX SAI FULL SAI	GET TABLE START ADDR  SAVE AO  IS A LOCAT AVAILABLE  IS AI MAX  STEP AI IS AI EQUA TO AO STEP AI	1837 1495 1268 1775 1612 8001 1579 1584 1691 1749 1502 1751 1859 1662	15 20 16 20 60 44 60 15 16 45 15	1290 1915 1020 1804 1687 9975 1583 1687 1394 1502 1906 1804 1662 1915	1495 1268 1775 1612 8001 1579 1584 1691 1749 1754 1612 1859 1914 1612
MAX	LDD STD RAL	ON SW Al	SAI	SET SW ON SET AI TO	1754 17 <b>1</b> 2 1737	69 24 65	1909 1584 1240	1712 1737 1612
FULL SEX	HLT LDD STD RAL ALO	0222 H9XXX DRUMT EXIT 1IXXX	SEX 8002	DRUM PAKED SET DRUM TAG TO 9 STEP EXIT	1914 1318 1870 1466	01 69 24 65	0222 1216 1212 1725	1318 1870 1466 1820
YES	SCT SLT SRT STL SUP SLT AUP	0000 0001 0001 XXXX2 8003 0004 8001 SRP	8002	RESERVE LOCATION FOUND SAVE P	1820 1583 1660 1717 1823 1516 1873 1683	36 35 30 20 11 35	0992 0000 0001 0001 1962 8003 0004 8001	1660 1717 1823 1516 1873 1683 1791
8002 SCA 8001	SRT RAU ALO LDD SDA	0009 8003 AI ST ERAS	SCA 8001	NULIFY SCT	1791 8002 1722 1629 1841 1847	15 30 60 15 69 22	1494 0009 8003 1687 1544 1901	8002 1722 1629 1841 1847 8001
CA	STUP SUP SRTV MPY ALUP MPY ALUP ALUP	9972 8001 A1 0004 41 500 8003 8001 XXXX2 50 8003	CA	CALCULATE LOCATION FOUND	8001 1825 1733 1545 1710 1426 1552 1760 1767 1817	21 11 16 30 14 19 15 11 10 19	9972 8001 1240 0004 1616 1679 8003 8001 1962 1920 8003	1825 1733 1545 1710 1426 1552 1760 1767 1817 1340 1897
F OFF A1P1 AMAX AMP1 ON	SUP 00 RAL RAU RAU RAU RAL	8001 0000 AI A0002 A0200 A0201 AI	EXIT GDA SWOF TA TA TA SWON	CONSTANTS	1897 1634 1444 1290 1394 1906 1909	11 00 65 60 60 65	8001 0000 1687 0601 0799 0800 1687	1725 1851 1691 1579 1579 1579

A1 SRP ST 4I 500 50 FON AO AI BP1 EXIT	RAU SRT STU 00 00 00 RAL 01 01 01 HED	A0001 0000 9999 0000 0000 0000 8001 0000 0000	TA SCA CA 0004 0500 0050 STEPX AO AI BP1 EXIT	ERASEAB <b>L</b> E	1240 1494 1544 1616 1679 1920 1602 1804 1687 1915 1725	60 30 21 00 00 00 65 01 01	0600 0000 9999 0000 0000 8001 0000 0000 00	1579 1722 1825 0004 0500 0050 1810 1804 1687 1915 1725
		SUB 12	SET BLANK	<b>L</b> 8				
SUB12	ALO ALO STL	P0010 87TH P0010	8003		09 <b>9</b> 1 1891 <b>17</b> 99	15 15 20	198 <b>6</b> 1594 1986	1891 1799 8003
8 <b>7</b> TH	00	0000	8000	CONSTANT	1594	00	0000	8000
		SUB 13	CAL OPTIMU	JM DYNAMIC ADR				
SUB13	DD5LTOTOTL23TLLVUOUTLTOX	OPREG	I SEO SEO R8	STORE EXIT D OR I D I SHIFT TEST XAS TEST SAVE EV OD IS BASE EVEN OR OD	1964 1970 1923 1860 1867 1925 1581 1639 1875 1910 1716 1822 1947 1766 1277 1332 1689 1816 1922 1729 1972 1941	45556550523505405455054 29631313699326161436311	1416 1923 1452 00001 80002 00001 80003 14662 00061 17702 1961 1971 1971 80002 80008 1380 1380	1970 1875 1860 1867 1925 1581 1639 1947 1910 1716 1822 1947 13389 1816 1972 1972 1972 1941 1327
	DIV RAL	50 8 <b>0</b> 03	EXITZ	MOD 50	1327 1390	14 65	1380 8003	1390 1416
SHOP	RAL SRT SLT SLO TLU ALO	P0007 0004 0009 8002 U0001 GU	8002	TLU UNITS DIGIT OF D ADDRESS	1666 1887 1997 1917 1975 1812	65 30 35 16 84 15	1983 0004 0009 8002 1250 1866	1887 1997 1917 1975 1812 8002

8002 TOP	RAL SLT LDD BD3 SLT	9969 0002 OPTIM 0004	TOP SEO SEO	IS OP SRD	8002 1973 1779 1862 1916	65 35 69 93 35	9969 0002 1452 1916 0004	1973 1779 1862 1947
XAS 8002 NXT	RAL SRT SLT SLO TLU ALO RAL SLT	P0007 0004 0006 8002 10001 GXD 9968 0004	8002 NXT SEO	TLU D ADDRESS	1772 1937 1198 1966 1476 1912 8002 1274	65 30 35 16 84 15 65 35	1983 0004 0006 8002 1300 1967 9968 0004	1937 1198 1966 1476 1912 8002 1274 1947
U0001 U0002 U0003 U0004 U0005 U0006 U0007 U0008 U0009 U0010	00 10 20 30 40 50 60 70 80 90	2322 0706 0706 0908 1110 1312 1514 1716 1918 2120	2524 0706 0908 1110 1312 1514 1716 1918 2120 2322	SHIFT OPTIMIZING TABLE	1250 1251 1252 1253 1254 1255 1256 1257 1258 1259	00 10 20 30 40 50 60 70 80 90	2322 0706 0706 0908 1110 1312 1514 1716 1918 2120	2524 0706 0908 1110 1312 1514 1716 1918 2120 2322
10001 10002 10003 10004 10005 10006 10007 10008	19 79 80 80 80 90	9906 9907 0008 0106 0209 0308 5908	0600 0700 - 0800 0600 0800 0900 0800	X ACCUM ADD SUB TABLE	1300 1301 1302 1303 1304 1305 1306 1307	19 79 80 80 80 80 90	9906 9907 0008 0106 0209 0308 5908 9909	0600 0700 0800 0600 0800 0900 0800 0900
GU GXD 50	RAL OO HED	0000 0000 0000	T OP NXT 0050	CONSTANTS	1866 1967 1380	65 65 00	0000 0000 0000	19 <b>73</b> 12 <b>7</b> 4 0050
		SUB 14 P	UNCH AVAI	L TABLE				
SUB14	HED STD LDD STD LDD STD RAL	P EXITX 85TH P0010 A1 XXXX1 RS1	LOOP	STORE EXIT SET PUNCH 8 INIT AI INIT RS	1403 1849 1368 1739 1595	24 69 24 69 24	1196 1652 1986 1292 1961	1849 1368 1739 1595 1418
LOOP 8001 NEXT	AUP STL SUP NZU AUP ALO	C1 9999 TW1 C2 C3	8001 NEXT SRS 8003	SET LOCAT	1418 1829 8001 1702 1374 1377	65 10 20 11 44 10 15	1324 1382 9999 1468 1377 1430 1338	1829 8001 1702 1374 1528 1335 8003

SRS 8002 8003 TP PUN	STAUDO STAUDO STAUDO STAUDO STAUDO STAUDO ALCHO STAUDO ALCHO STAUDO STAU	XXXX2 C4 XXXX1 9998 9997 TW2 C5 C6 P0001 AIM AMP1 XXXX1	8002 8003 TP PUN 8002 EXITX	SAVE RS ' STORE AV TABLE  IS AI MAX STEP AI	1528 1518 1879 8002 8003 1752 1474 1427 1385 1578 1477 1435 1438 1248	20 60 15 69 21 44 10 15 16 15 16 15 16 15 16 16 16 16 16 16 16 16 16 16 16 16 16	1962 1424 1961 9998 9997 1618 1427 1480 1388 1977 1530 14342 1961	1518 1879 8002 8003 1752 1474 1578 1385 8002 1477 1435 1196 1248 1668
	RAL SLO	XXXX2 C7	LOOP		1668 1718	65 16	1962 1524	1718 1829
C1 C2 C3 C4 C5 C6 C7 TW1 TW2 85TH A1 RS1 AMP1	STL STD STD OO STL STD OO LDD LDD HED	P0001 P0009 0500 P0002 P0010 0001 1499 P0007 P0008 0080 A0001 0000 A0200 A0201	NEXT 0500 TP TP 0000 1499 NEXT TP 0000 8003 0450 8003 8003	CONSTANTS	1382 1430 1338 1424 1480 1388 1524 1468 1618 1652 1292 1324 1530 1342	20 20 00 24 24 00 20 20 69 69	1977 1985 0500 1978 1986 0001 1499 1983 1984 0080 0600 0799 0800	1702 1702 0500 1752 1752 0000 1499 1702 1752 0000 8003 0450 8003
		SUB 15	HEADING F	ROUTINE				
SUB15	HED STD SLT NZU RAU RAU HED	H EXITY 8003 0008 DH 8001 0000H 8001	EXITY	SAVE EXIT IS C1 BLANK HEAD DONT HEAD	1663 1768 1574 1543 1348 1818 1298	24 69 35 44 60 10	1813 8003 0008 1298 8001 1329 8001	1768 1574 1543 1348 1818 1813 1813
		SUB 16	INITIALI	ZATION				
SUB16	STD RAL STD LDD RAL AUP LDD	EXITY I1 P0009 0000H	SUBR1 SUBR1	STORE EXIT  ZERO CARD  ZERO HEAD  ZERO SYM T  MAKE DRUM  AVAILABLE	1353 1868 1929 1488 1432 1485 1593 1802	24 65 21 24 69 65 10	1813 1624 1985 1329 1485 1538 1546 1918	1868 1929 1488 1432 1600 1593 1802 1600

	LDD STD LDD STD RAL AUP LDD	H8XXX DRUMT 14 SWSYM 15 H8XXX EXITY	SUBR1	SET DRUM TAG TO 8 SET SWSYM OFF UNDEFINE REGIONS	1918 1968 1674 1580 1724 1631 1774	69 24 69 24 65 10	1406 1212 1527 1664 1577 1406 1813	1968 1674 1580 1724 1631 1774 1600
I1 I2 I3 I4 I5 PACOF	00 00 11 01 00 BD6	S0001 A0001 1111 0111 1021 FINDI	E0200 A0200 1111 SYMOF 1049 FINDL	CONSTANTS	1624 1538 1546 1527 1577 1852	00 00 11 01 00 96	0000 0600 1111 0111 1021 1824	0599 0799 1111 1218 1049 1874
		SUB 17	CALC 800X	EQUIVALENT				
SB17D SB17I DI SDA OD EV EO ADD1 DEQ IEQ 8002 8003	HDDDDULDLVULLOELLO 0000	X DEQ IEQ XXXX1 EXITY XXXX2 XXXX1 2IXXX OD 8002 8003 XXXX2 XXXX1 IIXXX 0008 0009 0000 0000	DI DI SUB13 EV EO EO ADD1 EXITY EXITY SDA SDA 8002 8003	D EQUIV I EQUIV SAVE TAG STORE EXIT STORE 800X GET OPTIM DYNAM ADDR IS DY ADDR EVEN OR OD	1902 1854 1627 1526 1576 1626 1904 1676 1482 1535- 1386 1643 1726 1630 1681 1776 1924 1974 1588 1789	69 69 21 20 20 20 40 40 40 40 40 40 40 40 40 40 40 40 40	1924 1974 1961 1813 1962 1961 1771 1535 1588 1789 1962 1630 1961 0992 0009 0000 0000	1627 1627 1526 1576 1626 1964 1676 1386 1643 1643 1726 1813 1776 1813 1904 8003
	HED	SUB 18	INDEX ADDR	FCC				
INDEX	HED STD STL RAU SLT	W EX <u>ITY</u> XXXX1 1954 0007	THE TOWN	STORE EXIT STORE ADDR	1826 1876 1926 1976	24 20 60 35	1813 1961 1954 0007	1876 1926 1976 1693
D	LDD BD5 SLT SRT MPY LDD BD6	D 0002 0009 20001 EXITY DS	D	D OR I	1693 1677 1532 1680 1727 1684 1777	69 95 35 30 19 69	1813 1680 0002 0009 1014 1813 1730	1677 1532 1680 1727 1684 1777 1582

DS	SRT ALO	0001 XXXX1	DS EXITY	CORE	1582 1 <b>7</b> 30	30 15	0001 1961	1730 1813
EXITY	01 HED	0000	EXITY	ERASEABLE	1813	01	0000	1813
		SUB 19	PROCESS	LOCATION				
	REG REG REG REG	D0923 G0925 J0928 K0931 L0933	0923 0925 0928 0931 0933	PROC L				
PROCL	HED STD RAU NZU LDD BDO	A EXITX 1951 NB DRUMT B	D0002	STORE EXIT IS L BLANK BLANK TEST DRUM TAG	0969 1899 1827 1632 1877	24 60 44 69 90	1196 1951 1731 1212 1781	1899 1827 1632 1877 0924
B SETL	RAL SLT LDD	OPREG 0004 P0008	SETL	BLANK SET L TO OR	1781 1927 1987	65 35 69	1872 0004 1984	1927 1987 1638
NB	SDA LDD	P0008 G	EXITX SUBR4	WHAT IS L	1638 1731	22 69	1984 1734 1957	1196 1213 0931
G0001 G0002 G0003	RAL LDD LDD	1957 K J	K0001 SUBR9 SUBR6	ABSOLUTE REGIONAL SYMBOLIC	0925 0926 0927	65 69 69	1780 1830	1174 1275
K0001 K0002	LDD RAU	5 L 87TH	SUBR5	TEST RANGE REG ERROR	0931 0932	69 60	1784 1585	1750 1839
X	AUP STU	P0010 P0010	^ SETL	SET BLANK L8	1839 1392	10 21	198 <b>6</b> 198 <b>6</b>	1392 1927
L0001 L0002	STL AUP	OPREG 86TH	SETL 800X	DRUM ADDR 800X	0933 0934	20 10	18 <b>72</b> 1 <b>6</b> 88	1927 1743
L0003	L DD RAU	ORCEQ 87TH	89X X	CORE ADDR OTHER	0935 0936	69 60	1738 1585	1442 1839
800X 89X	LDD STD	ORXEQ OPREG	89X <b>X</b>	O TITELY	1743 1442	69 24	1596 1872	1442 1839
J0001 J0002	RAL RAL	EQUIV D	K0001 SUB11	SYM DEFIND SYM UNDEF	0928 0929	65 65	1223 1682	0931 1801
J0003 D0001	RAU STL	87TH OPREG	X SS	S TAB FULL S EQ FOUND	0930 0923	60 20	1585 18 <b>7</b> 2	1839 1628
D0002 SS	RAU AUP	87TH HSYMB	X	DRUM PAKED STORE	0924 1628	60 10	1585 1230	1839 1635
	ALO LDD	LSYMB B	SUBR <b>7</b>	SYMBOL	1635 1880	15 69	1575 1781	1880 1279
G	00	0000	G0001	CONSTANTS	1734	00	0000	0925
K J	00	0090 0000	K0001 J0001		1780 1830	00 00	0090 0000	0931 0928
L	00	0000	L0001 D0001		1784 1682	00 00	000 <b>0</b> 0090	0933 0923
86TH 8 <b>7</b> TH	00	0008 0000	0000		1688 1585	00 00	0008 0000	0000 8000
EXITX	01	0000	EXITX	ERASEABLE	1196	01	0000	1196

## HED

## SUB 20 PROCESS DATA ADDRESS

	REG REG REG REG REG	D0937 F0939 G0941 J0944 K0946 L0949	0937 0939 0941 0944 0946 0949	PROC D				
PROCD  D0001 D0002 BT  X  NB G0001 G0002 G0003 J0001 J0002 L0001 L0002 L0003 L0004 800X	H S R N R S R L B S A S L R L L L R L S L S L S S S	B EXITX 1952 NB D ORBAL 88TH OPTIM EXITX OPREG PO010 G 1958 J K L 8 TH A XD 0004 P0007 P0004	SUB11 L0004 X EXITX EXITX SUBR4 J0001 SUBR9 SUBR6 SUBR5 X INDEX 800X INDEX SD	STORE EXIT IS D BLANK  FIND OPTIMUM D DRUM PAKED IS OP A BRANCH D TO OR SET BLANK  WHAT IS D ABSOLUTE REGIONAL SYMBOLIC TEST RANGE REG ERROR DRUM ADDR 800X CORE ADDR OTHER SET 800X D	0972 1949 1678 1737 0938 1778 1778 1783 1398 1592 1831 0944 0945 0945 0945 0945 0945 0951 0952 1836 1486	26450091001959999595920 2645266921266666663636230	1196 1952 1885 1492 1545 1545 1986 1878 1986 1987 1987 1987 1987 1987 1987 1987 1987	1949 1678 1732 1801 0952 1398 17783 1196 1213 0944 11275 1398 1826 1878 1826 1936 1486 1498
SCEQ K0001 K0002 K0003	AUP SLT LDD SRT LDD STL RAL RAL	XEQ 0004 P0007 P0007 0004 CEQ ORCEQ EQUIV F 88TH	SUB13 BT J0001 SUB11 X	GET 800X E SET CORE D GET CORE E SAVE CORE SYM DEFIND SYM UNDEF S TAB FULL	1498 1931 1642 1536 1586 1548 1832 0946 0947	10 35 69 20 65 65 60	1881 0004 1983 1983 0004 1782 1738 1223 1882 1542	1902 1642 1536 1586 1548 1964 1728 0944 1801 1398
F0001 F0002 SS	STL RAUP ALO LDD RAD SRT SRT	EQ 88TH HSYMB LSYMB EQ P0007 P0007 0004	SUBR7 L0004	SAVE EQ DRUM PAKED STORE SYMBOL SET D ADDR	0939 0940 1696 1735 1932 1785 1930 1636 1686	20 60 10 15 69 65 69 22 30	1793 1542 1230 1575 1785 1793 1983 1983 0004	1696 1398 1735 1932 1279 0952 1636 1686 1728

D G J F K AXC AXC XEQ CEQ 88TH	00 00 00 00 00 00 00 5TL 00	0088 0000 0088 0000 0088 0000 0088 0098 0RXEQ 0008	D0001 G0001 J0001 L0001 F0001 K0001 D0001 SCD BT SCEQ 0800	CONSTANTS	1685 1834 1645 1448 1882 1646 1828 1928 1881 1782 1542	00 00 00 00 00 00 00 00	0088 0000 0088 0000 0088 0000 0088 0098 1596 0008	0937 0941 0944 0949 0939 0946 0937 1931 1728 1832 0800
EQ	01 HED	0000	EQ	ERASEABLE	1793	01	0000	1793
		SUB 21	PROCESS	INSTR ADDRESS				
	REG REG REG REG REG	D0953 F0955 G0958 J0960 K0962 L0965	0953 0955 0958 0960 0962 0965	PROC I				-
PROCI	HED STD RAU NZU RAU NZU LDD BDO	C EXITX 1953 NB 1952 DNB DRUMT	G000 <b>2</b>	STORE EXIT IS I BLANK IS D BLANK D I BLANK TEST DRUM	1453 1833 1883 1838 1933 1938 1884	24 60 44 60 44 69 90	1196 1953 1788 1952 1888 1212 1988	1833 1883 1838 1933 1938 1884 0959
DNB D0001 D0002 X	RAL RAL STL RAL ALO	ORBAL D ORBAL P0010 89TH	L0004 SUB11 L0004 X	TAG FIND BEST FOUND DRUM PAKED	1988 1888 0953 0954 1742	65 65 20 65 15	1492 1692 1492 1986 1695	0968 1801 0968 1742 1934
NB F0001 F0002 F0003 J0001 J0002 L0001 L0002 L0004	STL LDD RDD LDD LDD LDD LDD LDD LDD LDD LD	P0010 F 1959 J K L P0010 AXD P0007 AXC P0007	EXITX SUBR4 J0001 SUBR9 SUBR6 SUBR5 X INDEX 800X INDEX MOR	WHAT IS I ABSOLUTE REGIONAL SYMBOLIC TEST RANGE REG ERROR DRUM ADDR 800X CORE ADDR OTHER	1934 1788 0955 0956 0957 0960 0961 0965 0966 0967 0968 1886	20959999599933	1986 1792 1959 1835 1985 1986 1736 1983 1983	1196 1213 0960 1174 1275 1750 1742 1826 1886 1936
800X 90XX	SIA AUP LDD SIA LDD	P0007 XEQ P0007 P0007 CEQ	SB17I SUB13	GET 800X E SET CORE ADDRESS GET CORE E	1786 1889 1939 1989 1440	23 10 69 23 69	1983 1842 1983 1983 1843	1889 1854 1989 1440 1964
K0001	RAL	EQUIV	J0001	SYM DEFIND	0962	65	1223	0960

K0002 K0003 G0001 G0002 SI	RAL RAL LDD RAL SIA AUP ALO LDD STL	G P0010 P0007 P0010 P0007 HSYMB LSYMB MOR ORBAL OPREG ORCEQ	SUB11 X SI X SUBR7 EXITX MOR	SYM UNDEF S TAB FULL FOUND BEST DRUM PAKED  STORE SYMBOL  SET OR TO ORB SAVE CORE	0963 0964 0958 0959 1540 1590 1640 1690 1936 1745	65 69 65 23 10 15 69 24 20	1490 1986 1983 1986 1983 1230 1575 1936 1492 1872 1738	1801 1742 1540 1742 1590 1640 1690 1279 1745 1196
D F J K L G 89TH AXC XEQ CEQ	00 00 00 00 00 00 00 00 STL 00 HED	0089 0000 0089 0000 0089 0000 0089 0099 ORXEQ 0009	D0001 F0001 J0001 K0001 L0001 G0001 0080 L0004 90XX MOR SCEQ	CONSTANTS	1692 1792 1835 1885 1935 1490 1695 1736 1836 1842 1843	00 00 00 00 00 00 00 00 20	0089 0000 0089 0000 0000 0089 0000 0089 1596 0009	0953 0955 0960 0962 0965 0968 0080 0968 1939 1936 1740
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